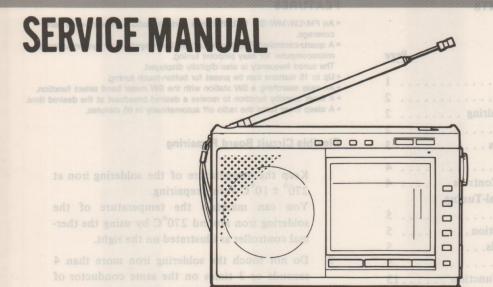
ICF-7600DA/7700



AEP Model UK Model E Model AUS Model ICF-7600DA: US Model anadian Model

SPECIFICATIONS

Circuit system

Frequency range

Antennas

Speaker Power output Outputs

Power requirements

FM: Superheterodyne

LW/MW/SW: Dual conversion superheterodyne FM: US, Canadian model: 76.0-108.0 MHz AEP, UK, E, AUS model: 87.5-108.0 MHz

MW: 530-1,700 kHz LW: 150-285 kHz

SW (1-12 SW broadcast bands): 3,050-26,100 kHz

FM/SW: Telescopic antenna MW/LW: Built-in ferrite bar antenna Approx. 7.7 cm (31/8 inches) dia. 400 mW (at 10% harmonic distortion) Recording output jack (minijack) output level 0.775 mV (-60 dB)

output impedance 1 kilohm Earphone jack (minijack) for 8 ohm earphone

6 V DC

Four size AA (R6) batteries

DC IN 6 V jack accepts: Sony AC-D4S AC power adaptor (optional) for use on 120 V

AC, 60 Hz

Sony DCC-127A or DCC-240 car battery cord (optional) for use with 12 V or 24 V car battery, respectively Sony EBP-6 battery case (optional) for use with four size C

(R14) batteries.

Battery life

Weight

Supplied accessories

Approx. 191.5 × 117 × 31.5 mm (w/h/d) (75/8 × 45/8 × 11/4 inches)

including projecting parts and controls 607 g (1 lb 53/8 oz)

including batteries Earphone (1)

Short wave guide (1) Carrying case (1) SW compact antenna (1)

Approx. 19 hours of listening for four hours a day at a normal volume, using Sony batteries SUM-3 (NS)

> FM/LW/MW/SW PLL SYNTHESIZED RECEIVER SONY



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	Replacing Chip Components
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NOTE: Removal and Block Diagram Sections have been omitted.

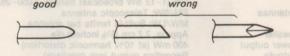
FEATURES

- An FM/LW/MW/SW 1-12, 15 bands portable radio with world-wide band coverage.
- A quartz-controlled PLL (Phase Locked Loop) synthesizer system using a microcomputer for easy pinpoint tuning.
 The tuned frequency is also digitally displayed.
- Up to 15 stations can be preset for button-touch tuning.
- · An easy searching a SW station with the SW meter band select function.
- · A timer standby function to receive a desired broadcast at the desired time.
- · A sleep timer turns the radio off automatically in 65 minutes.

Flexible Circuit Board Repairing

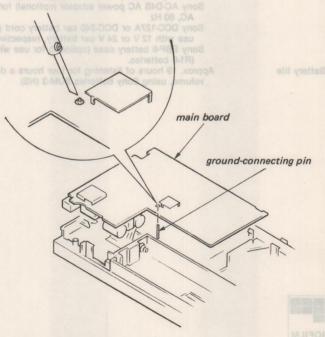
- Keep the temperature of the soldering iron at 270° ± 10°C during repairing.
 You can maintain the temperature of the
 - soldering iron around 270°C by using the thermal controller as illustrated on the right.
- Do not touch the soldering iron more than 4 seconds or 3 times on the same conductor of the circuit board.
- 3. Do not apply force on the conductor when soldering or unsoldering.

Tip of soldering iron



MAIN BOARD REMOVAL

Unsolder the ground-connecting pin at the main board as shown below.



FM/LW/MW/SW
PLL SYNTHESIZED RECEIVER

SECTION 1

ANILITUOTEM 10201, PP01716G-529.

Replacing chip components

All chip components should be connected and disconnected, using a tapered soldering iron [temperature of the iron tip: less than 280°C (536°F)], a pair of tweezers and braided wire.

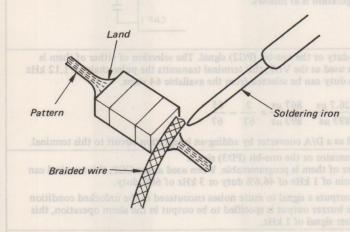
Precautions for replacement

- Do not disconnect the chip component forcefully.
 Otherwise, the pattern may peel off.
- 2. Never re-use a disconnected chip component. Dispose of all old chip components.
- 3. To protect the chip component, heating time for attaching the component should be within 3 seconds.

O Removing chip components

(1) Removing solder at electrode

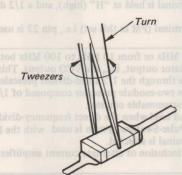
Remove the solder at the electrode, using a thin braided wire. Do not remove the solder of the part (chip component) attached adjacent to the electrode.



(2) Disconnecting chip components

Turn the tweezers with the soldering iron alternately applied to both electrodes, and the chip component will be disconnected. Take careful precautions while disconnecting, because if the chip component is forcefully removed the land may peel off.

Rever re-use a disconnected chip component.



(3) Smoothing the soldered surface

After disconnecting the chip component, remove the solder by using a braided wire to smooth the land surface.

O Connecting chip components

The value of chip components is not displayed on the main body. Take due precautions to avoid mixing new chip components with other ones.

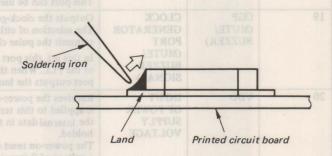
(1) Applying solder to land on one side

Apply a thin layer of solder to the land on one side where the chip component is to be connected. Too much solder may cause bridging.



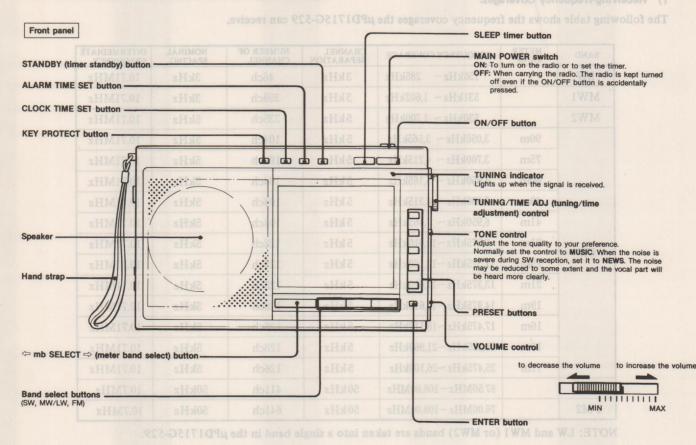
(2) Speedy soldering

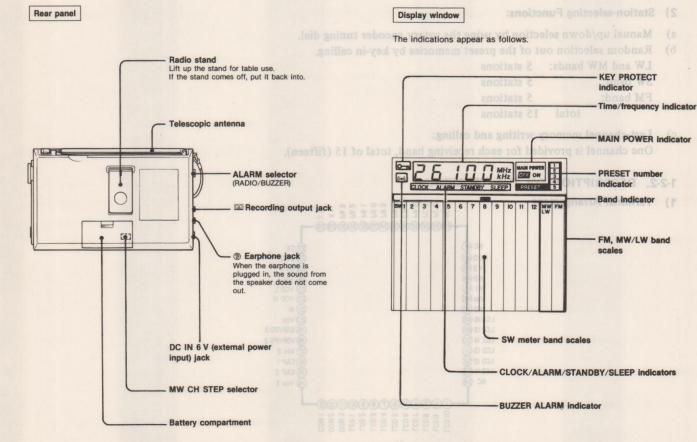
Hold the chip component at the desired position, using tweezers, and apply the soldering iron in the arrow-marked direction. To protect the chip component, heating time should be within 3 seconds.



(3) Speedy soldering of electrode on the other side Solder the electrode on the other side in the same way as in (2) above.

1-1. LOCATION AND FUNCTION OF CONTROLS





1-2. OUTLINE OF THE C-MOS DIGITAL-TUNING SYSTEM IC201, µPD1715G-529.

1-2-1. OUTLINE OF THE STATION-SELECTION

1) Receiving-frequency Coverages:

The following table shows the frequency coverages the μ PD1715G-529 can receive.

BAND	METER BAND	FREQUENCY COVERAGE	CHANNEL SEPARATION	NUMBER OF CHANNEL	NOMINAL SPACING	INTERMEDIATE FREQUENCY
LW	nen carrying the even if the ON	150kHz~ 285kHz	3kHz	46ch	3kHz	10.71MHz
MW1	.0000	531kHz~ 1,602kHz	3kHz	359ch	3kHz	10.71MHz
MW2	F button	530kHz~ 1,700kHz	5kHz	235ch	5kHz	10.71MHz
	90m	3,050kHz~ 3,565kHz	5kHz	104ch	5kHz	10.71MHz
	75m	3,700kHz~ 4,215kHz	5kHz	104ch	5kHz	10.71MHz
	60m	4,650kHz~ 5,165kHz	5kHz	104ch	5kHz	10.71MHz
	49m	5,800kHz~ 6,315kHz	5 kHz	104ch	5kHz	10.71MHz
	41m	6,950kHz ~ 7,465kHz	5 kHz	104ch	5kHz	10.71MHz
CW	31m	9,375kHz~10,010kHz	5kHz	128ch	5kHz	10.71MHz
SW	25m	11,525kHz~12,160kHz	5kHz	128ch	5kHz	10.71MHz
	21m	13,375kHz~14,010kHz	5kHz	128ch	5kHz	10.71MHz
	19m	14,975kHz~15,610kHz	5kHz	128ch	5kHz	10.71MHz
	16m	17,475kHz~18,110kHz	5kHz	128ch	5kHz	10.71MHz
	13m	21,325kHz~21,960kHz	5kHz	128ch	5kHz	10.71MHz
	11m	25,475kHz~26,100kHz	5kHz	126ch	5kHz	10.71MHz
FM1		87.50MHz~108.00MHz	50 kHz	411ch	50kHz	10.7MHz
FM2	1	76.00MHz~108.00MHz	50kHz	641ch	50kHz	10.7MHz

NOTE: LW and MW1 (or MW2) bands are taken into a single band in the µPD1715G-529.

2) Station-selecting Functions:

- a) Manual up/down selection by using the rotary encoder tuning dial.
- b) Random selection out of the preset memories by key-in calling.

LW and MW bands: 5 stations

SW band: 5 stations FM band: 5 stations

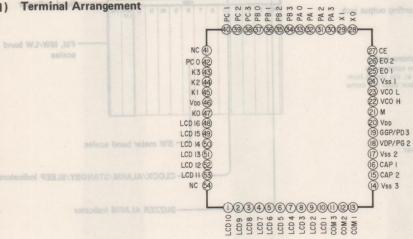
total 15 stations

c) Last-channel memory writing and calling:

One channel is provided for each receiving band, total of 15 (fifteen).

1-2-2. DESCRIPTION ON THE TERMINALS

1) Terminal Arrangement



2)

PIN Nr.	SYMBOL	NAMING	DESCRIPTION
ixing new	ns to avoid m	LCD SEGMENT SIGNALS SI	Transmit the segment-output signals to the LCD panel. When matrixes are configured together with the COM1 thru COM3, a display of 48 dots can be made. These output signals are output when the LCDD commands are made. The LCD-driving voltages are of 3.1V typical, 1/2 bias and 1/3 duty when the frame frequency is 100 Hz. These LCD11 through LCD16 can also be used at the same time as the key source signals for the key matrix. These signal are output on time-division bases, and they are output as the key-source signals at the repetition rate of 6.7 msec. Whether the key-source signal are to be output while having displays on the panel is dependent upon and selectable by the programs used. These terminals become automatically in the "L" (low) state, i.e., non-display mode, at the power-on reset (VDD changes from low to high state) and at the stoppage moment of the clock. The display mode does not change at the reset moment in which CE changes from low to high state.
	COM3 THRU COM1	LCD COMMON SIGNAL	Transmit common signals to the LCD panel. When the matrixes are configured together with the LCD1 through LCD16, a display of 48 dots can be made. Three distinctive signals of VSS3, VSS2 and VDD are output through these terminals at the repetition rate of 50 Hz. These terminals become automatically in the "L"(low) state, i.e., non-display mode, at the power-on reset (VDD changes from low to high state) and at the stoppage moment of the clock.
14 15 16 17	VSS3 CAP2 CAP1 VSS2	CAPACITOR CONNECTION TERMINAL FOR DOUBLER	Capacitor-connection terminals to make a proper voltage doubler to build the 3.1V typical LCD-driving voltage VDD. Normal circuit configuration is as follows.
	OUTPUT)	VARIABLE DUTY PORT (POWER- SUPPLY CONTROL SIGNAL)	Outputs the variable-duty or the one-bit (PG2) signal. The selection of either of them is programmable. When used as the VDP, this terminal transmits the pulse chain of 1.12 kHz continuously, and its duty can be selected from the available 64 steps. $\frac{26.7 \mu s}{893 \mu s} \sim \frac{867 \mu s}{893 \mu s} = \frac{2}{67} \sim \frac{65}{67}$
19	CGP (MUTE/ BUZZER)	CLOCK GENERATOR PORT (MUTE/ BUZZER SIGNAL)	This port can be used as a D/A converter by adding an integration circuit to this terminal. Outputs the clock-generator or the one-bit (PD3) signal. The selection of either of them is programmable. When used as the CGP, this terminal can transmit the pulse chain of 1 kHz of 46.6% duty or 3 kHz of 60% duty. In this set, this port outputs a signal to mute noises encoutered in the unlocked condition of the PLL. When the buzzer output is specified to be output in the alarm operation, this port outputs the buzzer signal of 1 kHz.
	ODV	INPUT OF POWER SUPPLY VOLTAGE	Receives the power-supply voltage for this device. In operation, a voltage of 2.2 to 3.5 VDC is applied to this terminal. The input voltage can be lowered down to 2.0 VDC when any of the internal data in the RAM, i.e., when the CKSTP command is under execution, is to be holded. The power-on reset circuit of device starts to operate at the instance this terminal receives a voltage of 0 (zero) to 2.0 VDC, and the program starts from the location 0 (zero). Note: This pin and pin 46 are connected internally. So, it is not necessary to apply the power-supply voltage to both of them. The ceramic-packaged device, however, has a not-to-be connected pin 46, i.e., N.C. terminal.
21 9/11/2 91	M (FMC)	CONTROL- SIGNAL INPUT FOR DIVIDER	Determines the dividing ratio of the fixed-division prescaler. A 1/4 dividing ratio is made when this terminal is held at "H" (high), and a 1/2 divider is made when held at "L" (low). This port is used only when the VCOH terminal (FM in this set) i.e., pin 22 is used. This set uses this port as a 1/4 divider.
22	VCOH (FM IN)	FM OSC SIGNAL INPUT	Receives frequencies from 10 MHz to 130 MHz or from 10 MHz to 100 MHz both of a level of 0.2 Vp-p minimum from the local-oscillator output, i.e., the VCO output. This input signal is connected internally in this device through the 1/2 fixed-divider prescaler or the 1/4 fixed-divider prescaler and through the two-module prescaler composed of 1/32 and 1/33 frequency dividers to the internal programmable counter. This terminal is pulled down to the ground level when the direct frequency-dividing system is taken into the circuit or when the Pulse-Swallow system is used with the HF command executed, i.e., the VCOL (AM) terminal is selected. A capacitor coupling is needed due to the inclusion of alternate current amplifiers inside this device.

PIN Nr.	SYMBOL	NAMING	DESCRIPTION DESCRIPTION					
23	VCOL (AM IN)	AM OSC SIGNAL INPUT	Receives an AM local-oscillator signals, i.e., the VCO signal from 0.5 MHz to 40 MHz 0.2 Vp-p minimum. This port is selected when the direct frequency-dividing system or the Pulse-Swallow is used and, at the same time, the HF command is executed. These two systems are, ever, different one another as shown below.					
	case of SW b	st frequency. In	DIVIDING SYSTEM	INPUT LEVEL (MINIMUM)	INPUT FRE- QUENCY	DIVIDING RATIO		
	cy. To that c	s lowest frequen	DIRECT WE MAY be	0.1 Vp-p	0.5 to 15 MHz	16 to (2 ¹² -1)		
	the following	he LCD displays	PULSE-SWALLOW (HF COMMAND EXECUTED)	0.2 Vp-p	`0.5 to 40 MHz	1,024 to (2 ¹⁷ -1)		
			This terminal is pulled dow together with the VHF-com A capacitor coupling is need this chip.	mand execution, i.e	., the VCOH (FM) te	rminal is selected.		
24	Vssl	GROUND	The ground-return terminal	of this device.				
25 26 ens semituon	E01 E02 margorq anti	ERROR OUTPUT ERROR OUTPUT	Transmits the error signal of dividing the local-oscillator (high)-level signal is output (low)-level signal is output with the reference frequence from this port then go throw tuned circuits in these from E01 is obtainable from the When the PLL is disabled, it terminal, pin 27, is set to the floating states.	frequencies are high from this port. When from this port. When ey, this port becomes ugh the external low tends of the receiver, next terminal EO2, p e.e., when the system	er than the reference in these are lower, on these divided freque in a floating state. The pass filter to the vara The same output water in 26, and so these the is set by the PLL con	frequency, an "H" the contrary, an "L" encies just coincide hese output signals actor diodes in the aveform as the terminal wo are user-selectable. mmands or the CE		
72 All PAO is	CE to revewed	CHIP ENABLE	Receives the state-selection When set at "H" (high), this becomes forcively in a disat (low)-level state. The durati The programmes are using t when the state of this CE is state, these programmes wo When the CKSTP command the internal clock generator the RAM-memory backupir maximum. In this condition COM1 COM3 become in the When the level of this CE te programmes start from the input mode.	s device works, and woled condition in the lon, however, of shorther CKSTP command in an "L" (low) control like under NOP college and the internal CP ags can be made under, these display-output of off-display mode, it eminal is changed from the condition of the lower control like the lower control	duration of wider the ter than 140 µsec is is. The CKSTP commidition. When this CE commands. this CE terminal is it used as a very-low current to tignals LCD1 through the "L" (low) statem "L" to "H", this com "L" to "H", this	nan 140 µsec of the "L" not taken into account. nands are effective only terminal is in an "H" nan "L" (low) level, is disabled condition, consumption of 3 µA ugh LCD16 and the te. device is reset and its		
28 29	XO XI	CRYSTAL OSCILLATOR	An external quartz-crystal of generator for the devices.	oscillator connects to	these terminals to o	btain 75 kHz signal		
30 THRU 33	PA3 THRU PA0	PORT A	4-bit I/O (Input/Output) po by bit. The each designation the "BANK 0 (zero)" in the	n is, in turn, perform e data memory (RAM	ed by the contents in I), called as a "PAIO	the location "1FH" in word". At these		
(30)	(K-ENTER)	(ENTER-KEY	instances as the power-on, c "L" to "H", these terminals	s automatically become	ne initial setting of the	e CE terminal from		
(31)	(DAT-R)	IN) (ENCODER DATA IN)	Under the port-operation of the PAO port coincides with	ommands like "IN",	"OUT", "SPB", "RF	B" commands, etc.,		
(32)	(REQ-R)	(ENCODER REQUEST)	PA3 coincides with the mos Port C respectively.	st-significant bit of th	nem. The same applie	es to the port B and		
(33)	(RES-R)	(ENCODER RESET)	In this set, the PA3 (K-ENT ENTER and alternate BUZZ	ZER keys and the ke	ese return signals fro y matrix is as shown	m the momentary below.		
				DAT-S	RES-R			
			K-ENTER	ENTER	BUZZER			
			In this set, PA2 through PA					

	SYMBOL	NAMING	DESCRIPTION
(cont'd)	gures and lette	tions with the fig	input signals: data signal from the rotary encoder and request signal from the rotary encoder
			output signal: reset signal from the rotary encoder
		LAYED	A partial circuit of the rotary encoder and these corresponding waveforms at each input or output terminal are those shown below.
			COM 3 COM 2 COM 1
			A D Q D AT-R COM2
		COM 1	MCOM3 R-23R R N 3 COM 2
		2	10 LCD1 CA bA aA 5 3
		MEMORY	9 LCD2 A LCD2 A LCD2
		ALARM	8 LCD3 ea PLAD
			7 LCD4 black MHz MHz
			6 LCD5 V A R-SIR NOTE (FM-SOKHZ)
			S LCD6 ce nwodbe qu
			4 LCD7 d3 gs fs (FM-0.1MHz)
4	PB3	PORT B	4-bit exclusive output ports.
HRU 7	THRU PB0		For ports PB0 and PB1, the sink current is minimized to a specially-low drain, so the PB0 and PB1 are able to be used as the return-signal sources for the key matrix. Accordingly,
34)	(LAT-B)	(LATCH	any of the reverse-current-preventive diodes can be eliminated when these PB0 and PB1
Igina Tinged		SIGNAL)	ports are used as the key-return-signal sources. When these ports are used as ordinal output ports, it will happen any of the low-level output signal will not be output properly depend-
5)	(LAT-A)	(BAND INFO.)	ing upon the circuit employed, because of the low sink current. In these cases, pull-down
3)	(LAI-A)	(LATCH SIGNAL)	resistors are used in these output lines.
		(FREQ.	Under the port-operation commands like "IN", "OUT", "SOB", "RPB" commands, etc., the PA0 port coincides with the least significant bit of the registor or the operand, the PA3
		POSITION DISPLAY)	coincides with the most-significant bit of them. The same applies to the Port B and Port C
			respectively. These evaluation entered and all 1 in it is it is a significant
(6)	(DAT-S)	(DATA	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on
		(DATA SIGNAL)	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H".
	(DAT-S) (CLK-S)	(DATA SIGNAL) (CLOCK	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under
36)		(DATA SIGNAL)	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required.
		(DATA SIGNAL) (CLOCK	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are
		(DATA SIGNAL) (CLOCK	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows.
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7)	(CLK-S)	(DATA SIGNAL) (CLOCK SIGNAL)	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow.
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7) odt gnis	(CLK-S)	(DATA SIGNAL) (CLOCK SIGNAL)	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow.
cong the	(CLK-S)	(DATA SIGNAL) (CLOCK SIGNAL)	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB - MSB BUZZER SW L SWH ALARM DAT-S DATA BAND INFORMATION
(7)	(CLK-S)	(DATA SIGNAL) (CLOCK SIGNAL)	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB - MSB BUZZER SW L SWH ALARM DATA BAND INFORMATION CLOCK LATCH
ont gaing	(CLK-S)	(CLOCK SIGNAL)	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB MSB BUZZER SWL SWH ALARM DATA BAND INFORMATION CLOCK LATCH METER-BAND FREQUENCY SCALE FREQUENCY-POSITION DISPLAYS DISPLAYS DISPLAYS FREQUENCY-POSITION DISPLAYS
ont gains	(CLK-S)	(DATA SIGNAL) (CLOCK SIGNAL)	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB MSB BUZZER SW L SWH ALARM DATA BAND INFORMATION CLOCK LATCH DATA BAND INFORMATION CLOCK LATCH DATA BAND INFORMATION CLOCK LATCH DATA BAND INFORMATION DISPLAYS DISPLAY DISPLAY DISPLAY DISPLAY
(7)	(CLK-S)	(CLOCK SIGNAL)	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB MRSB BUZZER SWL SWH ALARM DATA BAND INFORMATION LAT-B LAT-B DATA BAND INFORMATION DISPLAYS DISPLAYS DISPLAYS DISPLAYS DISPLAYS DISPLAYS
cong the	(CLK-S) data by utilities the frequency of the frequency	(CLOCK SIGNAL) area to rennam bracketsm not	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB MSB BUZZER SWL SWH ALARM DATA BAND INFORMATION LAT-B METER-BAND DISPLAYS DATA FREQUENCY-POSITION DISPLAYS FREQUENCY-POSITION DISPLAYS FREQUENCY-POSITION DISPLAYS
sing the	(CLK-S)	(CLOCK SIGNAL) area to rennam bracketsm not	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB - MSB BUZZER SWL SWH ALARM DAT-S CLK-S DATA BAND INFORMATION CLOCK LATCH DATA FREQUENCY-POSITION DISPLAYS DISPLAYS DATA FREQUENCY-POSITION DISPLAYS DISPLAYS DATA FREQUENCY-POSITION DISPLAYS DISPLAYS DATA FREQUENCY-POSITION DISPLAYS DISPLAYS DATA CLOCK LATCH
ont gains	(CLK-S) data by utilities the frequency of the frequency	(CLOCK SIGNAL) area to rennam bracketsm not	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB + MSB BUZZER SWL SWH ALARM DATA BAND INFORMATION CLOCK LATCH DATA FREQUENCY POSITION DISPLAYS DATA FREQUENCY-POSITION DISPLAYS CLOCK LATCH FREQUENCY POSITION Tailing edge low level
7)	(CLK-S) data by utilities the frequency of the frequency	ATAGNIZ ATAGNIZ (CLOCK SIGNAL) Manner of serial state of seri	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB+-MSB BUZZER SWL SWH ALARM DAT-S CLK-S CLK-S DATA BAND INFORMATION BAND INFORMATION BAND INFORMATION Band Information CLOCK LATCH CLOCK LATCH CLOCK LATCH BAND INFORMATION Band Ingedge Leading edge Beading edge Beading edge
7) odt gnis	(CLK-S) data by utilities the frequency of the frequency	(CLOCK SIGNAL) area to rennam bracketsm not	respectively. These exclusive output ports should be initialized using the programmes, because these ports output unstable and undefined output signals at the initial power-on condition, i.e., from "L" to "H". The output-data contents are the same as these previous last ones when the CE terminal changes from "L" to "H" or from "H" to "L", and when the CKSTP commands are under execution. Accordingly, the initialization with programmes is needed too where required. In this set, these ports are used as those shown in the parenthesises. The 40-bit frequency-position displaying information and the 8-bit band information are sent out to external circuits as serial data flows. The data and the clock signals are used in common for the band information (for IC201) and for the frequency-position information. A partial block diagram is shown blow. BAND-OUTPUT CODE LSB — MSB BUZZER SWL SWH ALARM DAT-S CLOCK LAT-B METER-BAND METER-BAND FREQUENCY-POSITION DISPLAYS DATA FREQUENCY-POSITION DISPLAYS DATA FREQUENCY-POSITION DISPLAYS CLOCK LATCH FREQUENCY-POSITION DISPLAYS CLOCK LATCH FREQUENCY-POSITION DISPLAYS BAND INFORMATION leading edge leading edge Note: Frequency-position display turns on when the data is "11"

PIN Nr.	SYMBOL	NAMING	DESCRIPTION								
(cont'd)	the set does no	tandby condition.	ton solt	a) When the set is in	noitiba	by Co	bret2	to Ti	ON-OI	. v	STANDS
		clock time and alby condition, the		BAND (METERBAND)	BAND-O	Y)	T CO: → LS		SWH	SWI	(cont'd)
		des with the alarm		LW/MW1/MW2	0	0	1	0	Low	High	1
		get to the "BUZZ	th mas been	OTTOOR MISTALEAN	0			-			
	oximately 65 to	eep period of appr	e tor the s	SW (90m) SW (75m)	0	0	1 0	$\begin{bmatrix} 1 \\ 0 \end{bmatrix}$	Low	High	
		" is on in this con		SW (60m)	0	1	0	1	Low	High High	20
		M" switch S220 h condition, the se		CITT (10)	0	1	1	0	Low	High	
	Denni Grandi	ely 65 minutes.		SW (41m)	0	1	î	1	Low	High	
		KM OFF) switch	FF" (ALA	SW (31m)	1	0	0	0	High	Low	
		sound, the set turn			1	0	0	1	High	Low	
	does not turn	f. However, the set	le oinsi si	SW (21m)	1	0	1	0	High	Low	
		"STANDBY" key wand alarm functi		SW (19m)	1	0	1 0	$\begin{bmatrix} 1 \\ 0 \end{bmatrix}$	High	Low	
	A MARKET OF STREET	Marin's Institute Marin's		SW (13m)	1	1	0	1	High High	Low	
		dition with the "A	alum cor	SW (11m)	1	1	1	0	Low	High	
		ll of the radio fun	IU' side, h	FM1/FM2	1	1	1	1	Low	Low	
	then the "ON/I	imply turned on. V	si olbas					1	Low	Low	
	o norman Sun	set turns the alam	N	OTE: Also refer to 1	1-2-4. Descri	ption	On				
	Taran .		46.5	Displaying Fu	nction on la	ter pa	ages.				
THRU HOLDO	PC3 (KS3) THRU PC1 (KS1)	PORT C (KEY- MATRIX	Refer to for the F	the PORT B outlined at PORT B. et, these ports output sig	pove. All the p	ports					
38 THRU 40\00 40	PC3 (KS3) THRU	PORT C (KEY-	Refer to for the F In this so	the PORT B outlined at	pove. All the p	oorts o	he sign	nal son	urces for the	he key ma	strix.
38 THRU 40	PC3 (KS3) THRU PC1 (KS1)	PORT C (KEY- MATRIX SIGNAL- SOURCE OUTPUT)	Refer to for the F In this so	the PORT B outlined at PORT B. et, these ports output significant free terminal and is not as a junction land.	connected to	ports of	he sign	nal son	urces for the	he key ma	strix.
THRU 10 00 00 00 00 00 00 00 00 00 00 00 00	PC3 (KS3) THRU PC1 (KS1)	PORT C (KEY- MATRIX SIGNAL- SOURCE OUTPUT)	Refer to for the F In this so	the PORT B outlined at PORT B. et, these ports output sig	connected to	ports of	he sign	nal son	urces for the	he key ma	strix.
THRU Molno 40 and 10 an	PC3 (KS3) THRU PC1 (KS1) NC PC0 (KS0)	PORT C (KEY- MATRIX SIGNAL- SOURCE OUTPUT) NO PORT C KEY	Refer to for the F In this is a be used a Refer to	the PORT B outlined at PORT B. et, these ports output sign free terminal and is not as a junction land. description for pins 38 elusive input ports. These	connected to through 40 ou	the in	ternal above	circu	urces for the	ne key ma	al can
38 THRU 40 41 41 42 42 43 43 41 41 43	PC3 (KS3) THRU PC1 (KS1) NC PC0 (KS0) K3 THRU	PORT C (KEY- MATRIX SIGNAL- SOURCE OUTPUT) NO PORT C KEY INPUTS	Refer to for the F In this is a be used a Refer to	the PORT B outlined at PORT B. et, these ports output sign free terminal and is not as a junction land. description for pins 38 elusive input ports. These e KIN or the KI command.	connected to through 40 out are normally nds executed,	the in	ternal above as the condition	circu	its, and the	is termina	al can
THRU 41 42 43 GOLD AT THRU 145 GOLD AT T	PC3 (KS3) THRU PC1 (KS1) NC PC0 (KS0) K3 THRU K1	PORT C (KEY- MATRIX SIGNAL- SOURCE OUTPUT) NO PORT C KEY	Refer to for the F In this is a be used a Refer to 4-bit exc When the RAM da These poused as t key sour	the PORT B outlined at PORT B. et, these ports output sign free terminal and is not as a junction land. description for pins 38 elusive input ports. These	connected to through 40 out are normally ands executed, by the operane at the port C a rces. When the	the in utlined the cod portand the cod much	as the ordition of e LCD 9 these corrections.	circule.	its, and the matrix inposition of these pins of the can h LCD 16 and h LCD 16 and nding port	is termina ut termina are read ds. specifical are used a	al can als. into the ly be as the
THRU Molan and the second base and base	PC3 (KS3) THRU PC1 (KS1) NC PC0 (KS0) K3 THRU K1	PORT C (KEY- MATRIX SIGNAL- SOURCE OUTPUT) NO PORT C KEY INPUTS	This is a be used a Refer to 4-bit exc When the RAM da These poused as t key sour while dis Whether TKLF coa proper	the PORT B outlined at PORT B. et, these ports output significant free terminal and is not as a junction land. description for pins 38 elusive input ports. These e KIN or the KI commar ta memories designated to the series are so configured that he key-return signal sources, these keyed signals a	connected to through 40 out are normally and executed, by the operand the port C arces. When the are output from on the dispals are properly it is requisite t	the inutlined the cod portand the see LC om the play poly out to exe	as the ordinate of the LCD of the second and the second and the second and the second of the second	circure. key-ros of these of the services of	matrix input these pinse command 16 can h LCD 16 nding portradio.	is termina ut termina are read ds. specifical are used a every 6.7	als. into the ly be as the 7 msec
HRU moles and base an	PC3 (KS3) THRU PC1 (KS1) NC PC0 (KS0) K3 THRU K1	PORT C (KEY- MATRIX SIGNAL- SOURCE OUTPUT) NO PORT C KEY INPUTS	This is a be used a Refer to 4-bit exc When the RAM da These poused as t key sour while dis Whether TKLF coa proper source si	the PORT B outlined at PORT B. et, these ports output signated to the second of the se	connected to through 40 out are normally and executed, by the operand the port C arces. When the are output from on the dispals are properly it is requisite t	the inutlined the cod portand the see LC om the play poly out to exe	as the ordinate of the LCD of the second and the second and the second and the second of the second	circure. key-ros of these of the services of	matrix input these pinse command 16 can h LCD 16 nding portradio.	is termina ut termina are read ds. specifical are used a every 6.7	als. into the ly be as the 7 msec
THRU 10 10 10 10 10 10 10 10 10 10 10 10 10	PC3 (KS3) THRU PC1 (KS1) NC PC0 (KS0) K3 THRU K1	PORT C (KEY- MATRIX SIGNAL- SOURCE OUTPUT) NO PORT C KEY INPUTS	This is a be used a Refer to 4-bit exc When the RAM da These poused as t key sour while dis Whether TKLF co a proper source si Same as	the PORT B outlined at PORT B. et, these ports output signated to as a junction land. description for pins 38 et. lusive input ports. These et. KIN or the KI command ta memories designated to the sey-return signal sources, these keyed signals applaying keyed information the sex eyed signals applaying keyed information these keyed source signated the sex eyed so	connected to through 40 out are normally and executed, by the operand the port C arces. When the are output from on the dispals are properly it is requisite t	the inutlined the cod portand the see LC om the play poly out to exe	as the ordinate of the LCD of the second and the second and the second and the second of the second	circure. key-ros of these of the services of	matrix input these pinse command 16 can h LCD 16 nding portradio.	is termina ut termina are read ds. specifical are used a every 6.7	als. into the ly be as the 7 msec
THRU Molno as and base and bas	PC3 (KS3) THRU PC1 (KS1) NC PC0 (KS0) K3 THRU K1	PORT C (KEY- MATRIX SIGNAL- SOURCE OUTPUT) NO PORT C KEY INPUTS	This is a be used a Refer to 4-bit exc When the RAM da These poused as t key sour while dis Whether TKLF coaproper source as Same as Same as Same as	the PORT B outlined at PORT B. et, these ports output sign free terminal and is not as a junction land. description for pins 38 et. lusive input ports. These et. KIN or the KI commanta memories designated to the are so configured that he key-return signal sources, these keyed signals applaying keyed informati these keyed source signal summands. Accordingly, i command execution of gnal outputs. pin 20.	connected to through 40 out are normally and executed, by the operand the port C arces. When the are output from on the dispals are properly it is requisite t	the inutlined the cod portand the see LC om the play poly out to exe	as the ordinate of the LCD of the second and the second and the second and the second of the second	circure. key-ros of these of the services of	matrix input these pinse command 16 can h LCD 16 nding portradio.	is termina ut termina are read ds. specifical are used a every 6.7	als. into the ly be as the 7 msec

1-2-3. ON THE KEY MATRIX

1) On The Key Matrix TABLE

The key matrix of this set is configured as shown below.

	K3 (PIN 43)	K2 (PIN 44)	K1 (PIN 45)	K0 (PIN 47)
KS3 (PIN 38) (PC3)	PRESET 5 (S216)	PRESET 4 (S215)	PRESET 3 (S214)	PRESET 2 (S213)
KS2 (PIN 39) (PC2)	PRESET 1 (S212)	FM (S211)	LW/MW (S210)	SW (S209)
KS1 (PIN 40) (PC1)	ALARM (S208)	CLOCK (S207)	DOWN (S206)	UP (S205)
KS0 (PIN 42) (PC0)	STANDBY(S204)	KEY PROTECT (S203)	SLEEP (S202)	ON/OFF (S201)
DAT-S (PIN 36) (PB1)			FM/FM2* (JUMPER)	MW1/MW2* (DIODE)

DAT-S(PIN 36)(PB1) ENTER (S217) RES-R(PIN 33)(PA0) BUZZER** (S220)

K-ENTER (PIN 30) (PA 3)

* : initially set by diodes : alternate key

no mark: momentary keys

2) On The Initial-state Setting Diodes:

The FM-band frequency-coverage switching diode D202 and the MW-band channel-separation diode D203, i.e., the initial-state determining diodes are read only at the initial power-supply leading edge and at the moment when the CE (Chip Enable) terminal changes from "L" (low) to "H" (high) state. Diode D202 is solder bridge selected, and diode D203 is slide-switch selected by the MW CH STEP switch S218 as outlined below. Both of these selecting parts are installed on the key board. The solder bridging is factory selected for the specific destinations, and the MW CH STEP switch is selectable by the user from the battery compartment.

KEY NYENCY NYEN	"ENTER" key, the MOITONUA of the receiving fre			DESCRIPTION	
FM1/FM2 (solder-bridge)	FM band frequency coverage change (D202)	F	FM-band frequency coverage can be changed		d as follows.
	These "PRESET" and memory number displays on the		Bridge condition		rage
	when the rotary encoder is rotated and the receiving fi		shorted	87.50MHz -	108.00MHz
	changed, receiving band is changed or when the radio en one of these keys is pressed, the receiving band bec	iv	opened	76.00MHz -	108.00MHz
MW1/MW2 (MW CH STEP switch S218)	MW-band frequency coverage and channel separation changes (D203)	b	W-band frequence changed as follo	y coverage and channel s ws.	eparation can
	shortwave receiving spectrum is divided into 12 (twe-		S218	Coverage	Separation
	ds. Each band up to 41-meter band is further divided is, and each band up to 13-meter band is further divided		shorted	530kHz-1700kHz	5 kHz
	nnels. The 11-meter band is divided into 126 channel. When the 'TIP' key is pressed while receiving a shortw	aria	opened	531kHz-1602kHz	3 kHz
ively, When	receiving band changes to the next higher band success the SW12 (11-meter) band is reached, however, the bar				

3) On The Alternate BUZZER Key (\$220):

KEYaibai ban	to the highest band, (NOITONUF meter) band, The b		DESCRIPTION
the adjacent e lowest or the see.	Buzzer output/radio output changeover (S220)	(0	When the ALARM switch S208 is turned on, the ALARM switch S220 determines either of the buzzer or radio output. When S220 is shorted, the buzzer output is obtained and the LCD displays "BUZZER". When S220 is turned to RADIO, "BUZZER" display disappears.
last channel of meter) to SW5 an the channel	same channel number allocated for this receiver as the the last band. When the band is changed from SW6 (3) (41-metar) and the receiving channel has been higher the 104, however, the receiver receives the channel 104, Lo		The BUZZER-RADIO changeover can be made anytime. During the BUZZER on condition, however, the BUZZER-RADIO changeover (BUZZER to RADIO or RADIO TO BUZZER) cannot be performed, though the LCD display

4) On the Mometary Keys:

As outlined above in 1), Key Matrix Table, there are 17 (seventeen) momentary key switches. These key switches work under the following four conditions.

- a) Always seeing the input conditions.
- b) When being pushed, each key performs its function as a most-new information, and all the previous operations cease.
- c) First in, first served. The duplicate key pressings one after another at a time are prohibited. When a second or third key is or are pressed while a first key is kept pressed, these second or third key or keys are negrected.
- d) When two or more than three keys are just simultaneously pressed, a function of higher priority key is selected.

KEY		FUNCTION			DESCRIPTION			PIN Nr.
STANDBY (cont'd)	ON-OI	FF of Standby Condition	a)	When the set is in the no				
(cont a)	SWH		1 0	each other.	the clock time and a	narın tun	le comeide	WILII
			b	When the set is in the st	andby condition, the	alarmin	goperation	acts
	wol		ev	when the clock time coi				4000
	WAY		1 70	"ALARM" switch has b				utputs
	MoT			the alarming tone for th	e sleep period of app	roximate	ely 65 minu	ites.
	Low			When the "KEY PROTI				
	WOJ			released. When the "AL				
	Wol			"RADIO" side in the ab		et turns i	ts radio on	for the
	High		0)	sleep period of approximation when the "ON/OFF" (A		\$201 in	proceed du	ring the
	High		()	set is outputting the alar				
	daiH			and thus turns the radio				
	High			by function off. When t				
	High			condition, both the stan				
	High			thus the radio is turned				
	woll		d)	During the radio-alarm				
	WoJ			set to the "RADIO" sid				
				same as when the radio switch S201 is pressed,				
				turns the radio off.	the set turns the alar	ming run	ction on, a	nd also
			ig Fu	When the "STANDBY"	key is pressed in the	above co	ondition, th	e set
				turns the alarm and stan				
	aqqs J I		(e)	The alarm function acts	regardless of the cor	iditions of	of radio on/	
	urces for t		rola Ime	and during the alarm or	sleep operation, and	the LCD	displays	
			9	"SLEEP" on it.	SIGNAL			
SLEEP	Sleep-t	time Setting		In any mode with the "				
				displays "SLEEP" on it				
	its, and t		Jon zi	becomes in the sleep op tion of 65 minutes. In the				
				from the LCD, and the				pears
			2)	When the "SLEEP" key				o furns
			-/	on first and then the sle			(0830)	
				When the "SLEEP" key			ration, the	sleep
				time should be re-set. W				
				operation, the alarm fur	ection is turned off a	nd the sl	eep operation	on 8
				becomes effective. When the "ON/OFF (A)	ADM OFF)" from in	become	during the	laan
				operation, the sleep ope				
				turns off.	W I I I I I I I I I I I I I I I I I I I	dino oii	, and the ra	
KEY	Key Pr	otection	T	he key protection is made	by using the "KEY	PROTEC	T" kev.	
PROTECT	or the K		T	he on and off of the key-p				y as
	uoo puga		u	e "KEY PROTECT" key				
				ROTECT" key is pressed a				
				her key entries including		rol (rota	ry encoder)	are
				cepted than the "KEY PF his IC, however, accepts the		E tormin	al nin 27 1	Whon
				e CE terminal is turned of				
				rned off, the key protecti			JAHI	URH
ON/OFF	Radio	On-Off, (Alarm Off)		ne on and off of the radio			y pressing t	he
(ALARM OFF)	Switch		"(ON/OFF (ALARM OFF)"	key successively. W	hen the r	adio is turn	ed
				n, the set receives the freq				
				e memory. When the radi				
	Auto I			equency of now receiving				
				emories, and the display one of the present, i.e., of				
					set is configured a	N. S. Person	101-502-01	2 1152
		PRESET 3 (S214)						
		LW/MW (S210)			ESET 1 (S212)			

			standby condition, the alarmi		LCD pane	1.
iH			oincides with the alarm time. been set to the "BUZZER" s		10 ruque noss	AP CHARACTER
IH .			the sleep period of approxima		PIN Nr.	SYMBOL
进		When the "KEY PRO"	TECT" is on in this condition	, the alarm is	10	COMI
in I			LARM" switch S220 has been above condition, the set turns		13	COM 1
in		sleep period of approx		s its radio on for the	12	COM 2
Lo		c) When the "ON/OFF"	(ALARM OFF) switch S201		11	COM 3
Lo			arm sound, the set turns the		10	LCD1
Lo			io off. However, the set does the "STANDBY" key is pres		9	LCD2
oJ Lo			andby and alarm functions are			
od		thus the radio is turne			8	LCD3
iH			condition with the "ALARMide, all of the radio functions		7	LCD4
o.l			o is simply turned on. When t		6	LCD5
		1. 1. 00011	, the set turns the alarming fu		5	LCD6
		turns the radio off.			4	LCD7
		when the STANDBY	"key is pressed in the above andby functions off, and also	turns the radio off		
y to the			ts regardless of the conditions		3	LCD8
the lenv		and during the alarm of	or sleep operation, and the LC		2	LCD9
for our		"SLEEP" on it.	SIGNAL		anf PBI	LCD10
	Sleep-time Setting		"MAIN POWER" switch S21		53	LCD11
			it when the "SLLEEP" key is peration. The set turns off af		DI GOO GALO	Transport of the second
this term			this condition, the "SLEEP"		52	LCD12
		from the LCD, and the	e sleep operation turns off or		ote 51 man	LCD13
		a) when the SLEEP Ke	y is pressed in the radio-off s	tate, the radio turns	and, the FA3	100 off 10
man trees			leep function is initiated.	poration the clean	. 50	LCD14
			When the "SLEEP" key is pro		49	LCD15
1000		operation, the alarm for	unction is turned off and the			
			ALADM ODDANA	111111	48	LCD16
			ALARM OFF)" key is pressed or turns of the second of turns of the second of turns of the second of t			
by the T	Key Protection	The second secon	le by using the "KEY PROTE	ECT" key.		
mico M D	it is requisite to execute the Kt of the is	The on and off of the key	-protection state is made avai	ilable cyclickly as		1: The segn
nimatio			y is pressed successively. Whe		NOTE	2: The segn
			l and the key mark is displayed the "TUNING" control (rot		2) Description	on On the
		accepted than the "KEY		tary encoucity are		
		This IC, however, accepts	the switching at the CE term		The freque	ERCH.
			off, i.e., the "MAIN POWER"	" switch S219 is	LAT-A, DAT-	
	D- 11- 0- 066 (41 066	turned off, the key protec		53 LCD11	turn, made by	the LCD
FF)	Radio On-Off, (Alarm Off) Switching		io is made effective cyclically)" key successively. When the		a) Band an	d Meterbar
,	5 H 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		equency written in the last-ch			
		the memory. When the ra-	dio is turned off, this IC write	es the band and	FM	I MW/LW 90n
			ig in the last-channel location on the LCD changes from the			1111/211 3011
			f the instance the ON/OFF k		LSB	
		moled amade	an homesthese of the children	and the second of the	b) Frequen	cy-position
		snown octow.				
					1	2 3 4
					LSB	
			PRESET 1 (S212)			
		** : alternate key			DAT	-S - DATA
					CLK-	S - CLOC
			BUZZER** (\$220)			^
					LAT	-μ
		- 12 -				
		-				

1-2-4. DESCRIPTION ON DISPLAYING FUNCTION 1) Format of the Output Signals for the LCD: The following table shows the LCD segment/common output signals in conjunctions with the figures and letters on the LCD panel. FUNCTION FIGURES/LETTERS DISPLAYED COM 3 COM 2 COM COM 2 COM 1 COM 3 MEMORY d_A g_A f A CLOCK ALARM gB ев MHz kHz b_B f_B b₈ NOTE 1 (FM-50kHz) g₈ C6 b6 a 6 (FM-0.1MHz)b4 output posts. a7 e6 (FM-1MHz) d₄ e₄ f₄ d₇ b₂ C₂ g₂ a₂ (FM-10MHz) d_2 e_2 f_2 NOTE 2 (FM-100MHz) an ON OFF STANDBY BUZZER KEY PROTECT SLEEP e_1 g_1 f_1 Designations of the 7 (seven) segments: gment f8 is also connected to the displaying segments a, c and d. gment a0 is also connected to the displaying segments d, e and g. External LCD Displays: ion information is output to the external LCD display in the manner of serial data by utilizing the CLK-S signals. The external LCD displays of the SW indication, meterband and the frequency are, in driver incorporating the shift registers. and Displays: m 75m 60m 49m 41m 31m 25m 21m 19m 16m 13m 11m 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | FREQUENCY-DISPLAY **OUTPUTTING SEQUENCE:** FM MW/LW LSB MSB LSB MSB 1. Muting turns on. A bit0 bit1 bit2 bit13 bit14 bit39 2. Outputs value of N of PLL. OCK LATCH FREQUENCY-POSITION DISPLAY

1-2-5. INITIAL-STATE SETTING			
1) Initial Power-on Setting: VDD = 0 V → 3.0 V, CE = Low → High			
The power-supply is reset after performing the initial power-on setting, and the fo			
a) The power-out terminal becomes in "L" (low) state, i.e., power off, and reads the			
b) Initializes the preset-memories contents of each band (FM, SW or MW) to its lower of the 90-meter band.	est frequency. Ir	case of SW bar	nd, to that
c) Initializes the last-channel memories contents of each band (FM, SW or MW) to it meter band in case of SW band.	s lowest frequer	ncy. To that of	the 90-
d) Sets the clock and alarm time to "0:00", and resets the "second" starting.			
e) Outputs the serial data to turn off all the frequency displays from the LCD, and t	the LCD displays	s the following	clock
This terminal is pulled down to the groung level when the Pulse-Swallow system . valqaib			
together with the VHF-command execution, i.e., the VCOH GM) terminal is selected. A capacitor coupling is needed due to the inclusion of alternate current amplifiers inside			
this chip.			
The ground-return terminal of this lev c.			
Transmitted local-oscillator frequencies are higher than the reference frequency, an "H"			
2) Backup Condition: alste galleoft a al semosed frog sidt, yoneapert conerster odd altiw			
from this port then go through the external lowpass filter to the varactor diodes in the tuned circuits in these frontends of the receiver. The same output waveform as wol = 30 i			
The backup state is made when the CE terminal becomes in "L" (low) state. In the			
intermittently performed to eliminate the current drain by using the "HALT" comm	nand. The opera	tion routine is a	s follows.
a) The PLL is disabled.			
b) All the ports are made into "L" (low) state.			
c) All the external LCD displays are turned off except the clock display. Note: The same operations are made in the radio-off state when CE is "H" (high).	In this condition	however only	PAO is
"H" (high), i.e., it is seeing the buzzer switch.		.,	2120 10
The programmes are using the CASTP commands. The CASTP continance are using the CASTP continance are using the cast of this CE is in an "L" (low) condition, When this CE terminal is in an "H"			
1-2-6. MUTING-OUTPUT TIMINGS			
the internal clock generator and the internal CPU are disabled. In this TIPTIOLITY			
The muting-output timings are classified as follows.			
1) Approximately 15 (fifteen) ms chattering waiting time at key-on instance.			
2) Range checkings and N-value calcuration, and the PLL-data outputting.			
0. 5	vs key-off detec	ctions and the C	
3) Data-transferring periods to the LCD display data and to the external LCD displa	jb, nej om deter		E-termina
checking.	CRYSTAL		
An external quartz-crystal oscillator connects to these terminals to obtain 75 kH gnishador for the devices.			E-terminal
checking. A checking of district connects of the device district of			
checking. A chemical oscillator connects to these ferminals to checking. A che			
checking. a) Radio On POWER-OUT (1) Checking And			
checking. a) Radio On POWER-OUT (1) (2) Approx. App			
a) Radio On POWER-OUT (1) (2)			
a) Radio On POWER-OUT (1) (2) Approx. Approx. 400ms Approx. 400ms			
checking. a) Radio On POWER-OUT (1) (2) Approx. Approx. 400ms ROTARY ENCODER			

3. Displays frequency.

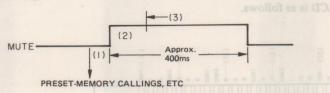
5. Displays dial scale.

4. Outputs band code. Muting is off during outputting.

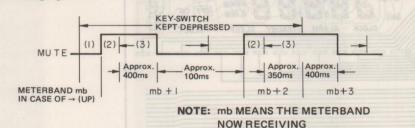
ALL PORTS BECOME IN

WHICH IS IN "H" (HIGH)

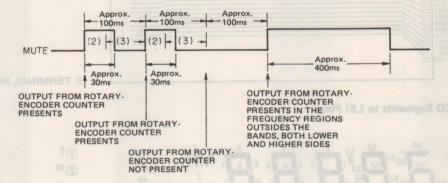
- c) Preset-memory Callings, Band Changings and Meterband Changings:
- c-1) One-time Key Hitting: had like the hood lab gallatoquosal lang yalqab labyus-bispil was a sass oibst sid!



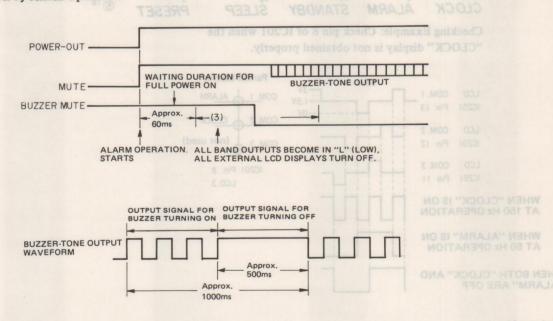
c-2) Meterband Changings with ← or → Switch Kept Depressed (Continuous/Successive Changings)



d) Station Selection with Rotary Encoder:

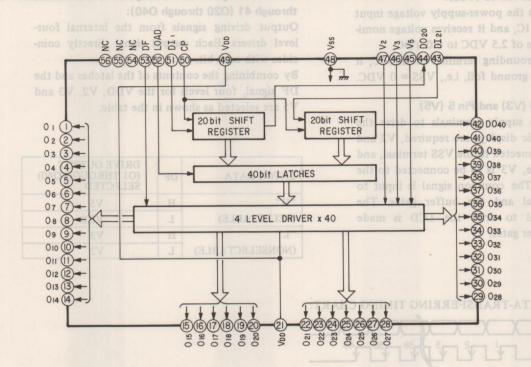


e) Power On by Alarm Operation



he LCD dial pointer moves up or down as the TUNING knob is rotated in 24 kHz in LW band, 20 kHz in SWI thru SW Hz in SW7 thru SWI2 and in 1.3 MHz step in FM band except these upper and lower band edges at where the point es irregularly as in the MW band.

1-3. OUTLINE OF TERMINALS OF THE LCD DOT-MATRIX SEGMENT DRIVER IC202, MSM5259GS



• Pin 51 (DI1)

Receives data from the shift registor of the first through twentieth bit, and accepts display data synchronizing with the clock signal in accordance with the truth values.

• Pin 50 (CP)

Receives the clock pulse of the shift registors, and the data are shifted at the trailing edge of the clock pulses. A setting-up and holding durations are required in between the DI1 mentioned above and this signal CP. The risetime and the falltime of the clock pulse are to be less than 1 μ sec.

Pin 44 (DO20)

Transmits the twentieth bit of the shift registor. Data received at the DI1 mentioned above are transmitted from this terminal being delayed with the duration of twenty bits of the shift registor and, at the same time, synchronized with the clock pulse.

When this terminal is connected to the DI21 terminal, pin 43, a 40-bit shift registor is made.

• Pin 43 (DI21)

Receives data of twenty-first through fourty-first bits of the shift registor. When this terminal is connected to the DO20 terminal, pin 44, as mentioned above, a 40-bit shift registor is made.

Pin 42 (DO40)

Transmits the fourtieth bit of the shift registor. Data received at the DI21 mentioned above are transmitted from this terminal being delayed with the duration of twenty bits of the shift registor and, at the same time, synchronized with the clock pulse.

When an expansion of handling the numbers of characters is needed, a cascading connection to the next stage is required.

• Pin 53 (DF)

Receives a signal to accommodate the alternatingcurrent synchronization for the waveforms of the LCD-driving signals.

Pin 52 (LOAD)

Input terminal to latch the contents of the shift registor. In the high ("H") conditions, the contents in the shift registor are transferred through the level shifter to the four level drivers.

In the low ("L") state on the contrary, this terminal retaines the last data of them in the high ("H") state, and thus the outputs from terminals 01 through 040 do not change even when the contents in the shift registors are changed.

ICF-7600DA/7700 ICF-7600DA/7700

Pin 49 (VDD) is the power-supply voltage input terminal of this IC, and it receives voltage nominally in the range of 2.5 VDC to 6.0 VDC. Pin 48 is the grounding terminal of this IC, it connects to the ground foil, i.e., VSS = 0 VDC.

Pin 7 (V2), Pin 6 (V3) and Pin 5 (V5)

The bias-voltage supply terminals to drive the LCD. When static displays are required, V2 and V5 are to be connected to the VSS terminal, and at the same time, V3 is to be connected to the VDD terminal. The common signal is input to the DF terminal and the buffer gates. The COMMON signal to drive the LCD is made through the buffer gates.

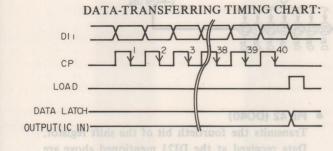
Pin 49 (VDD) and Pin 48 (VSS): Pins 1 through 20 (O1 through O20) and Pins 22 through 41 (O20 through O40):

Output driving signals from the internal fourlevel drivers. Each output signal directly coincides with each bit.

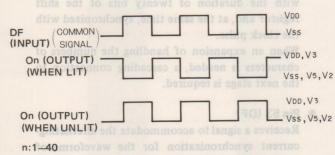
By combining the contents of the latches and the DF signal, four levels for the VDD, V2, V3 and V5 are selected as shown in the table.

LATCH DATA	DF	DRIVE OUTPUT (O1 THROUGH O40) SELECTED
"H"	Н	V5
(SELECTABLE)	L	VDD
"L"	Н	V3 (8) 10
(NONSELECTABLE)	L	V2

TIMING CHARTS



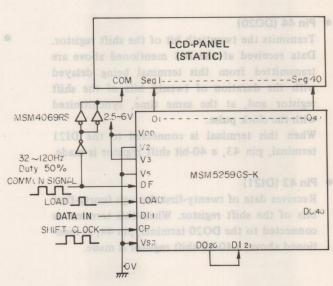
TIMING CHART IN STATIC FORM:



MSM5259GS-K 040

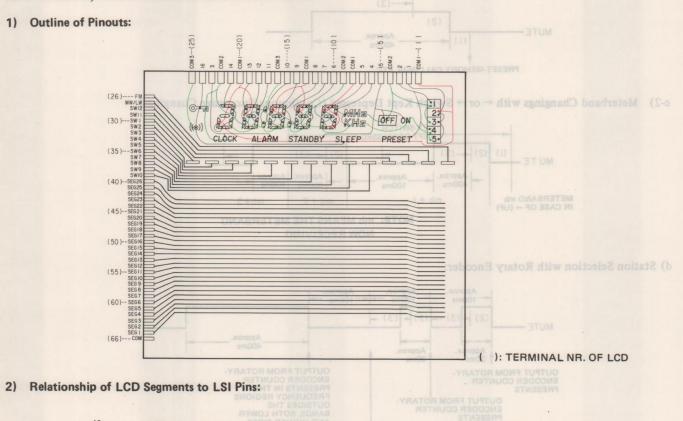
PARTIAL DISPLAYING CIRCUIT (EXAMPLE)

In case of the statical display, LCD-drive bias supplies V2 and V5 are to be connected to VSS, and further, V3 is connected to VDD terminal. Then, the common signal is input to the DF terminal, and also input to the common terminal of the LCD panel through the buffer gates.



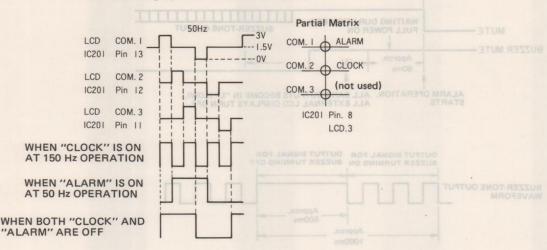
1-4. ON THE NEW LCD PANEL, LCD1

This radio uses a new liquid-crystal display panel incorporating dial pointer itself, band indicator, ordinal frequency and clock indicators, etc. The outline of this LCD is as follows.





Checking Example: Check pin 8 of IC201 when the "CLOCK" display is not obtained properly.



3) Dial-pointer Movement:

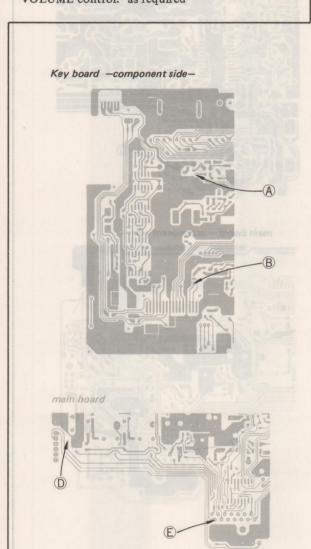
The LCD dial pointer moves up or down as the TUNING knob is rotated in 24 kHz in LW band, 20 kHz in SW1 thru SW6, 25 kHz in SW7 thru SW12 and in 1.3 MHz step in FM band except these upper and lower band edges at where the pointer moves irregularly as in the MW band.

SECTION 2 ELECTRICAL ADJUSTMENTS

PREPARATIONS:

Controls and switches should be set as follows unless otherwise noted.

POWER switch: ON
MAIN POWER switch: ON
TONE switch: as required
All key switches: as directed
VOLUME control: as required



Procedure:

- Adjust CT201 so that the reading on the frequency counter becomes in 10.860000 MHz (10.710 MHz + 0.150 MHz).
- Hit the FM button. The band should become in FM and the frequency on the LCD panel of the set should be 76.00 MHz (US, Canadian model) or 87.5 MHz (AEP, UK, E, AUS) model.
- Hit the SW button. The band should become in the SW1 and the frequency on the LCD panel of the set should be 3,050 kHz.

CLOCK-FREQUENCY ADJUSTMENT

NOTE: This adjustment is needed to these sets bearing serial numbers up to 7901.

These sets bearing serial numbers 7902 and

up are not equipped with the adjustment trimmer.

Refer to the mounting and schematic dia-

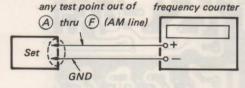
grams.

MAIN POWER SWITCH: ON

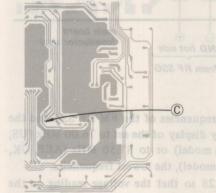
POWER SWITCH: ON

BAND: MW/LW

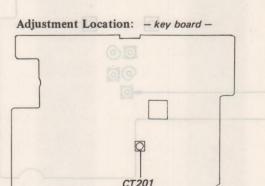
FREQUENCY: 150 kHz



key board -jumper-foil side-



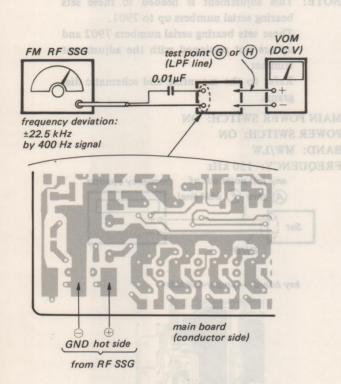
frequency the frequency of the FM RF SSC brand nism the highest confirm the highest confirmation.



FM FREQUENCY-COVERAGE (VCO VOLTAGE) ADJUSTMENT

Setup:

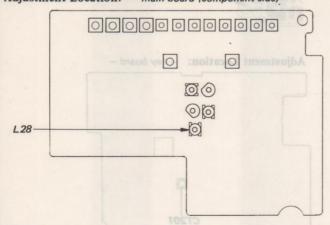
Band: FM

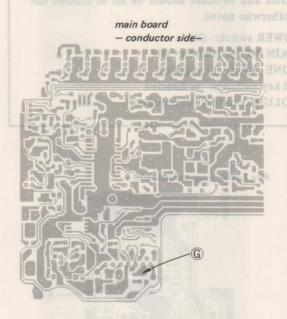


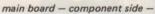
Procedure:

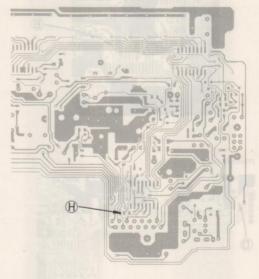
- Set the frequencies of the FM RF SSG and the frequency display of the set to 76.00 MHz (US, Canadian model) or to 87.50 MHz (AEP, UK, E, AUS, model), the lowest frequencies.
- 2. Adjust L28 so that the voltage reading on the VTVM becomes in 2.5 V \pm 0.1 V.
- Set the frequencies of the FM RF SSG and the frequency display of the set to 108.00 MHz, the highest frequency.
- 4. Confirm that the voltage reading on the VTVM is now between 11.5 V and 15.0 V.

Adjustment Location: - main board (component side) -









Adjust CT201 so that the reading on the frequency counter becomes in 10,860000 MHz

Hit the FM button. The band should become in FM and the frequency on the LCD panel of the

Hit the SW button. The band should become in the SW1 and the frequency on the LCD panel

NOTE: This adjustment is needed only for the set

bearing these serial numbers 7902 and up.

The adjustment control RV2 is not

equipped in these sets bearing the serial

(DC V range)

AM AGC ADJUSTMENT

Band: SW1

Procedure:

digital voltmeter.

Adjust RV2 to obtain a 1.275 V (±0.003 V) on the

Adjustment Location: - main board (component side) -

00

numbers up to 7901.

3-2. MOUNTING DIAGRAM (1) • Seepage 45-48 UNTIL SERIAL NO. 7901.

DC-DC CONVERTER CHECK

MAIN POWER switch: ON

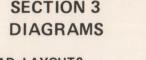
main board

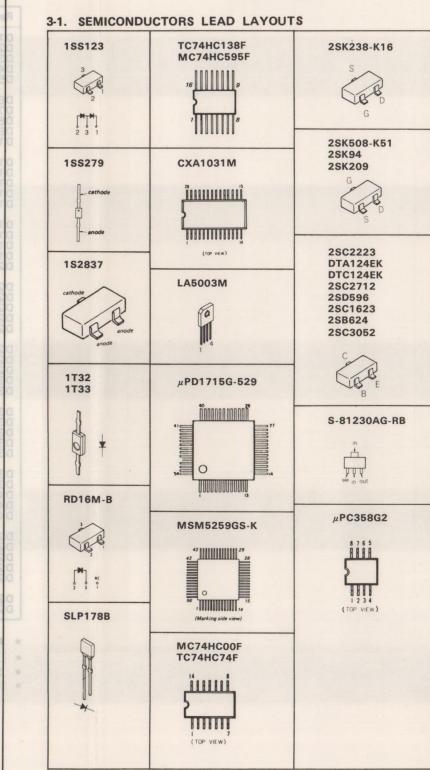
(DC V)

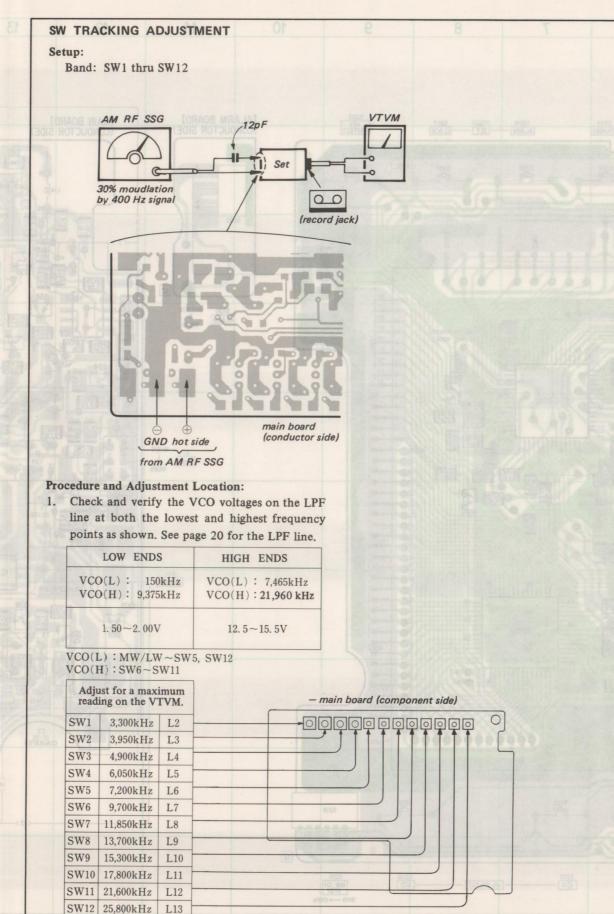
POWER switch: ON

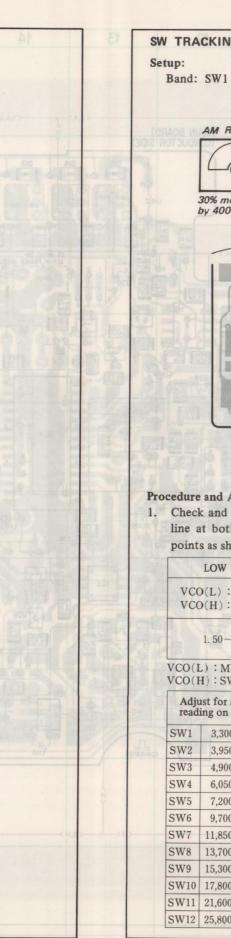
Procedure

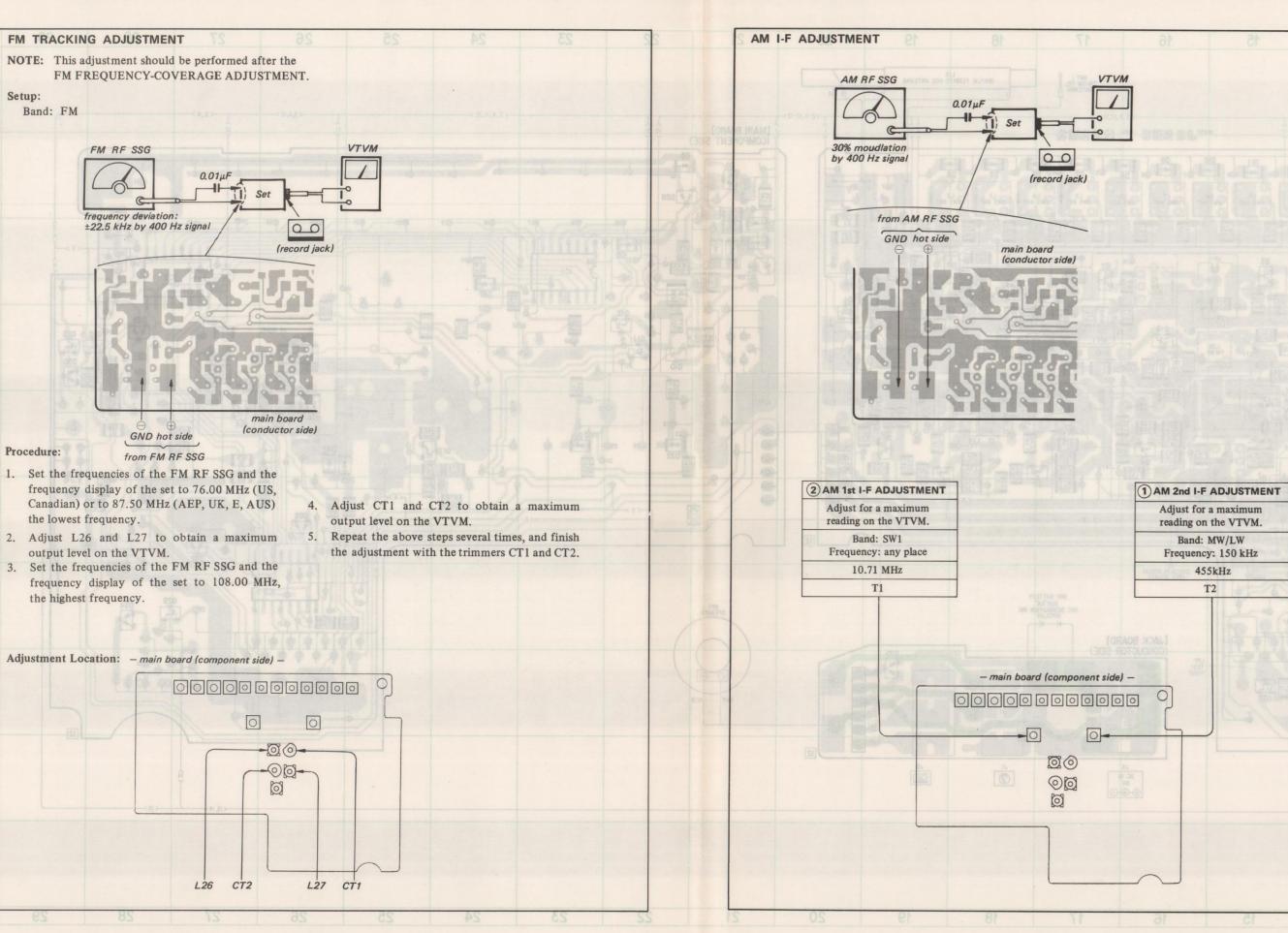
SECTION 3







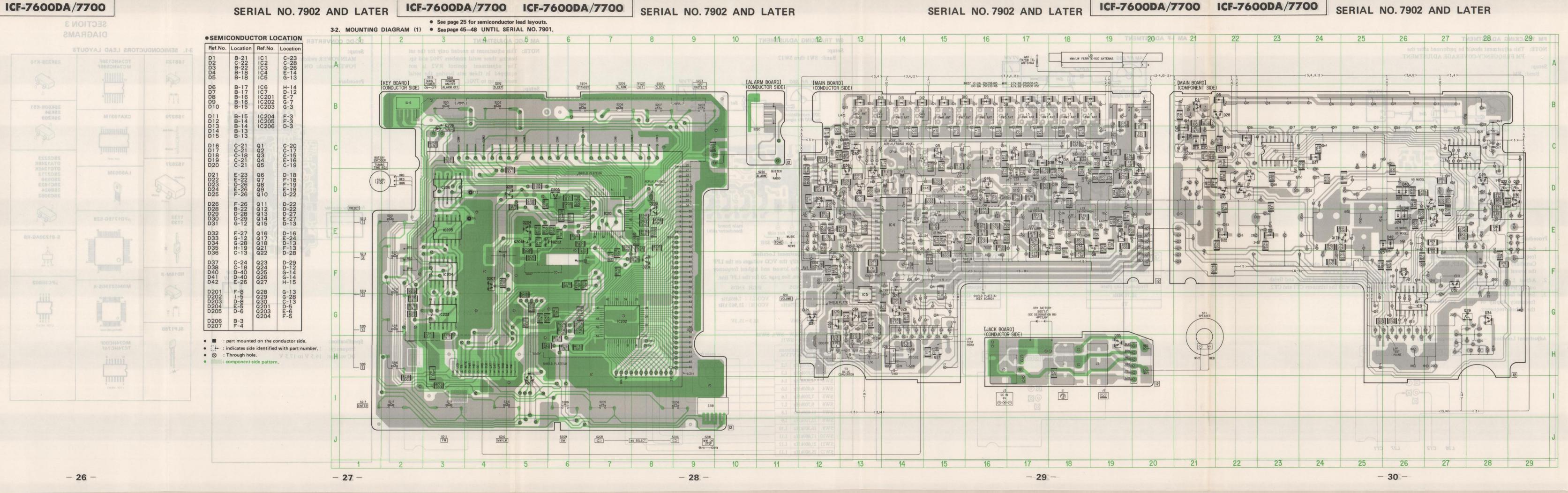


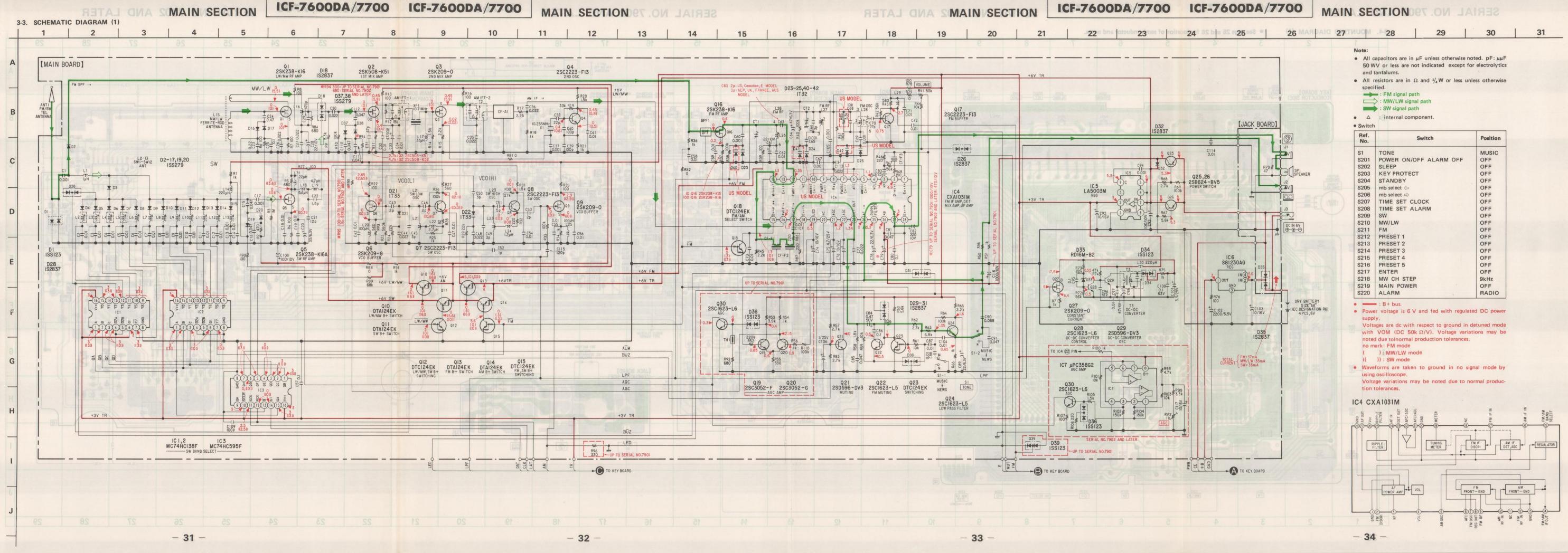


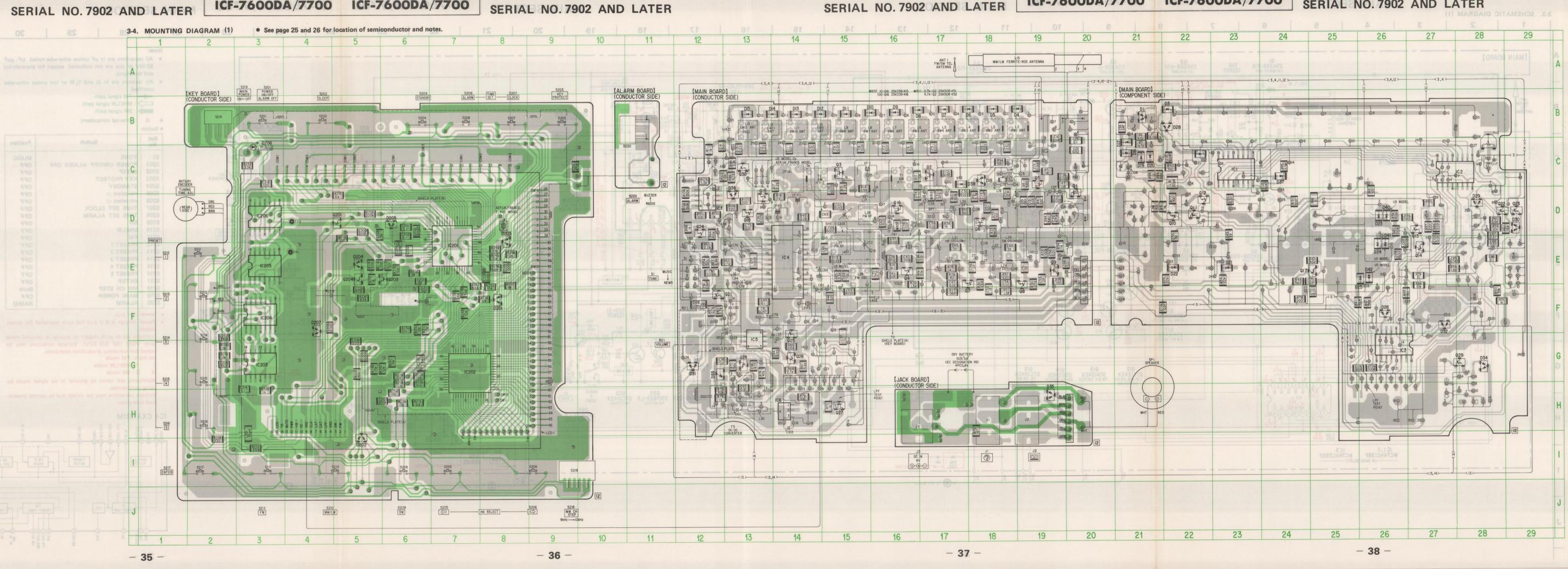
Specification:

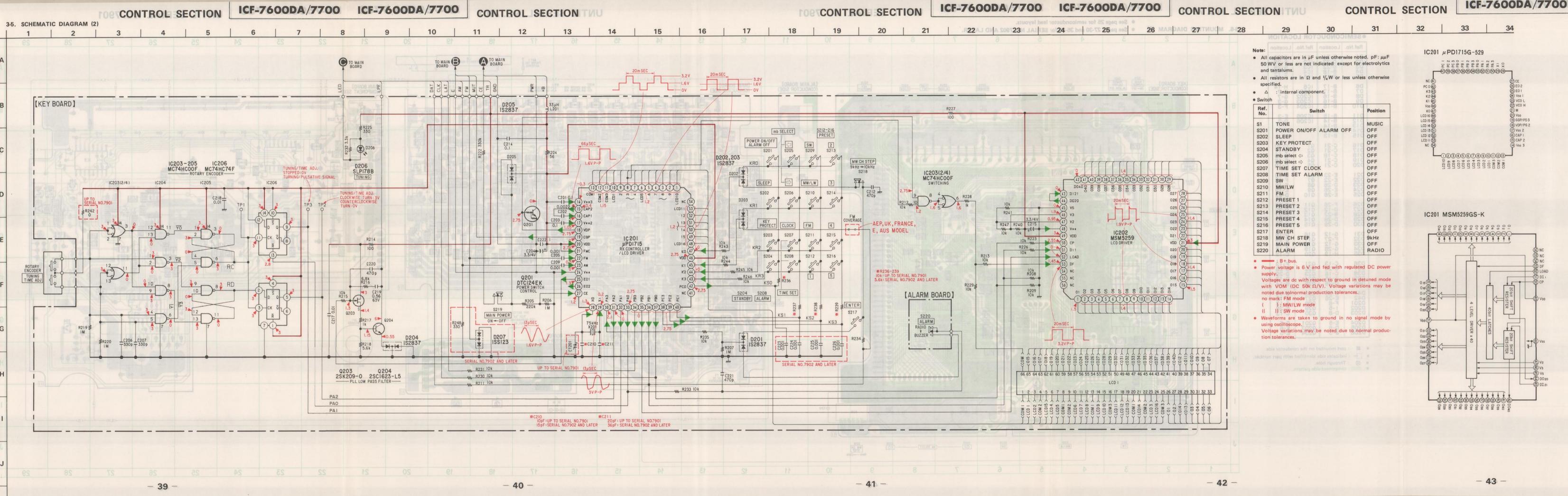
frequency: 1,750 kHz to 1,900 kHz

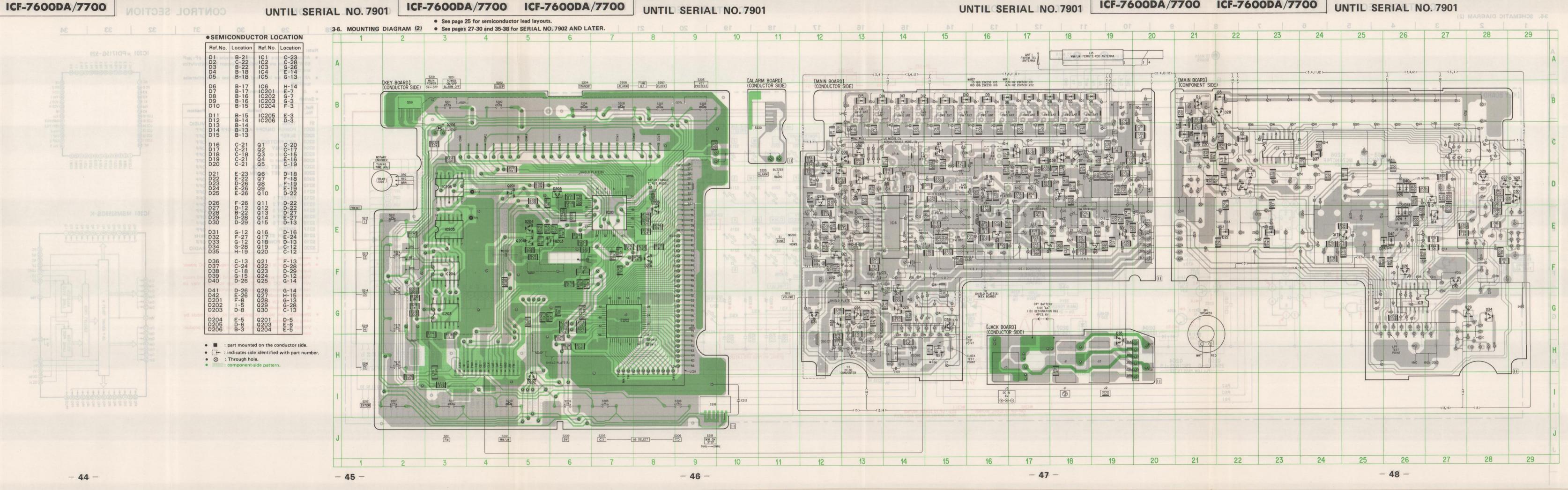
DC voltage: 16.5 V to 17.5 V







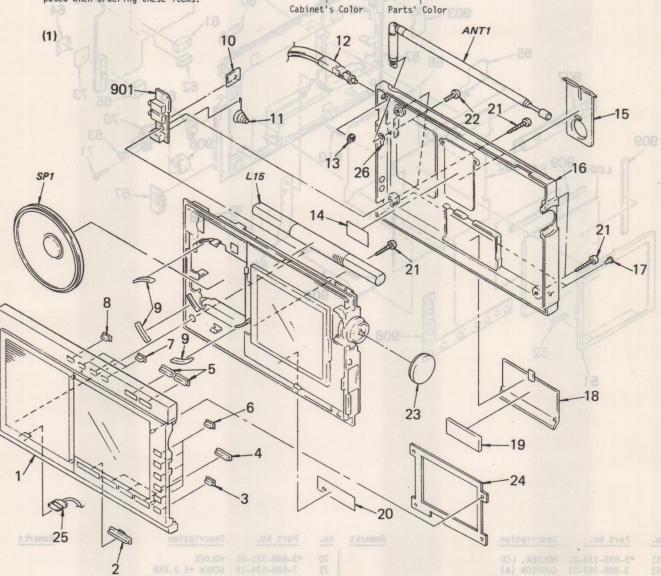




SECTION 4 EXPLODED VIEWS AND PARTS LIST

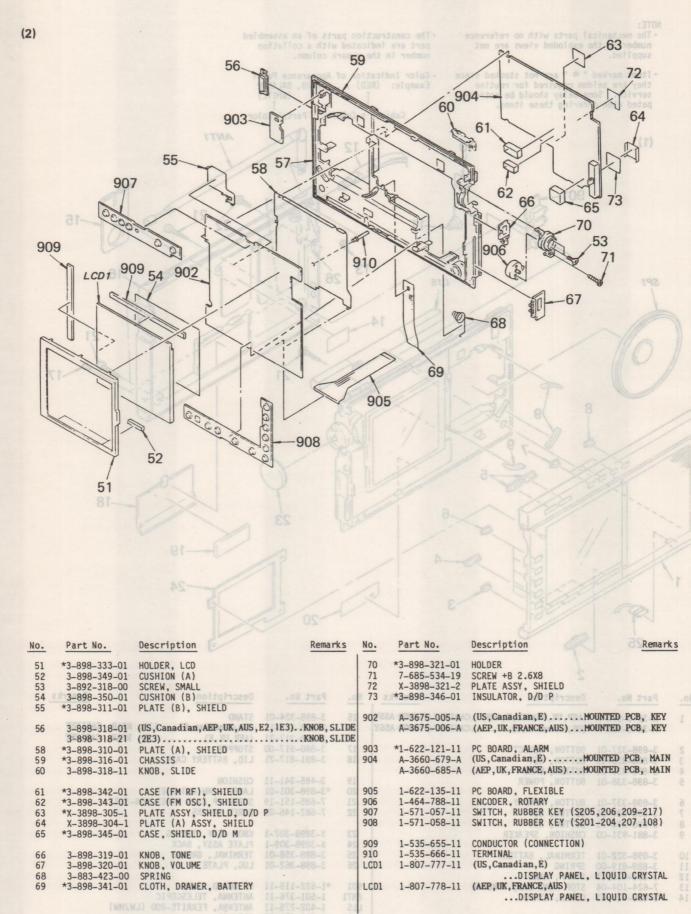
NOTE

- The mechanical parts with no reference number in the exploded views are not supplied.
- Items marked "*" are not stocked since they are seldom required for routine service. Some delay should be anticipated when ordering these items.
- The construction parts of an assembled part are indicated with a collation number in the remark column.
- Color Indication of Appearance Parts Example: (RED) KNOB, BALANCE (WHITE)



No.	Part No.	Description MOTALUZMI	Remarks	No.	Part No.	Description Remains	rks
1	A-3640-970-A A-3640-971-A	(AEP, UK, FRANCE, E, AUS) (US, Canadian, E)		15 16	3-898-324-01 3-898-317-11 3-898-317-21	STAND (US, Canadian, E)LID, REAR, CABINET (AEP, UK, E)LID, REAR, CABINET	
2	3-898-327-01 3-898-336-11	BUTTON, SET	03 *1-622-121-11 04 A-3660-679-A	17 18	3-880-917-00 3-891-817-21	STOPPER LID, BATTERY CASE	* 88
5	3-898-335-01 3-898-338-01	BUTTON, BAND BUTTON, POWER	A=3660-685+A 05 1-622-135-11		3-485-341-11 *3-898-301-01	CUSHION LABEL, (MW CH) STEP	
6 7 8	3-898-337-01 3-898-336-01 3-898-336-21	BUTTON, MEMORY BUTTON, SET BUTTON, SET	05 1-464-788-11 07 1-571-057-11 08 1-571-058-11	21 22	7-685-151-19 7-682-146-09	SCREW +P 3X20 TYPE2 NON-SLIT	52 * 53 *
9	3-881-931-00 3-898-322-01	CUSHION, SPEAKER TERMINAL, BATTERY	1-535-555-11 1-535-555-11	23 24 25	X-3898-307-1 X-3898-309-1 3-898-358-01	KNOB ASSY, TINING PLATE ASSY, BACK TERMINAL, GROUND	
11 12 13	3-889-819-00 3-891-813-11	SPRING STRAP, HAND STOP RING 2.0, TYPE -E	(01 1-807-777-11	901	3-898-362-01 *1-622-119-11	PC BOARD, JACK	
14	7-624-104-04 *3-898-323-01	COVER, STAND	12-011-100-1	ANT1 L15 SP1	1-501-376-11 1-402-275-11 1-502-631-00	ANTENNA, TELESCOPIC ANTENNA, FERRITE-ROD (LW/MW) SPEAKER	

SECTION 4 EXPLODED VIEWS AND PARTS LIST



SECTION 5 ELECTRICAL PARTS LIST

they are service.	seldom requir Some delay s	hould be antici- RESISTORS		UA: μA,	U: μ, for example: UPA; μPA, UPC:	Part	
pated wh	en ordering th	ese items. · All resistors are	in ohms.	UPD: µPD			RI
set such typical capacito	as a stereophor circuit parts ma	e same circuitsin a F: nonflammable nic machine, only COILS ay be indicated and sin other same F: nonflammable COILS . MMH: mH, UH: μΗ					
circuits	L No.7901)			(OI\)		-013-1	
	ELECTRIC	AL PARTS		ELECTRICA	-025-00 METAL CHIPM I		
Ref.No.	Part No.	Description 00-920-315-1 T28	Ref.No.	Part No.	Description		
901	*1-622-119-11	PC BOARD, JACK	C29 C30	1-163-033-00	CERAMIC CHIP 470PF CERAMIC CHIP 0.022MF	10% 10% 5%	50V 25V 50V
902	A-3675-005-A A-3675-006-A	(US, Canadian, E)MOUNTED PCB, KEY (AEP, UK, FRANCE, AUS) MOUNTED PCB, KEY	C31	1-163-109-00	CERAMIC CHIP 47PF CERAMIC CHIP 0.01MF	10%	50V
903	*1-622-121-11	PC BOARD, ALARM	C33 C34	1-123-822-00 1-163-021-00	ELECT 47MF CERAMIC CHIP 0.01MF	20% 10%	10V 50V
904	A-3660-679-A A-3660-685-A	(US, Canadian, E)MOUNTED PCB, MAIN (AEP, UK, FRANCE, AUS)MOUNTED PCB, MAIN	C35 C36	1-163-033-00 1-163-033-00	CERAMIC CHIP 0.022MF CERAMIC CHIP 0.022MF	10% 10%	25V 25V
905 906	1-622-135-11 1-464-788-11	PC BOARD, FLEXIBLE ENCODER, ROTARY SWITCH, RUBBER KEY (S205,206,209-217)	C37	1-163-141-00	CERAMIC CHIP 0.001MF	10%	50V 50V
907	1-571-057-11 1-571-058-11	SWITCH, RUBBER KEY (S201-204,207,108)	C39 C40	1-163-117-00 1-163-117-00	CERAMIC CHIP 100PF CERAMIC CHIP 100PF	5% 5%	50V 50V
909 910	1-535-655-11 1-535-666-11	CONDUCTOR (CONNECTION) TERMINAL	C41 C42	1-163-021-00 1-163-021-00	CERAMIC CHIP 0.01MF	10% 10% 0.25PF	50V 50V
ANT1	1-501-376-11	ANTENNA, TELESCOPIC	C43	1-163-085-00	CERAMIC CHIP 2PF		
BPF1 BPF1	1-235-253-00 1-235-254-00	(AEP,UK,FRANCE,AUS)FILTER, BAND PASS (US,Canadian,E)FILTER, BAND PASS	C44 C45 C46	1-161-055-00 1-163-086-00 1-163-085-00	CERAMIC O.022MF CERAMIC CHIP 3PF CERAMIC CHIP 2PF	20% 0.25PF 0.25PF	
C1 C2 C3	1-163-141-00 1-163-021-00 1-163-021-00	CERAMIC CHIP 0.001MF 10% 50V CERAMIC CHIP 0.01MF 50V CERAMIC CHIP 0.01MF 50V	C47 C48 C49	1-163-021-00 1-161-055-00 1-163-086-00	CERAMIC CHIP 0.01MF CERAMIC 0.022MF CERAMIC CHIP 3PF	10% 20% 0.25PF	50V 25V 50V
C4 C5 C6	1-163-021-00 1-163-021-00 1-163-021-00	CERAMIC CHIP 0.01MF 50V	C50 C51 C52	1-163-086-00 1-163-088-00 1-163-083-00	CERAMIC CHIP 3PF CERAMIC CHIP 5PF CERAMIC CHIP 1PF	0.25PF 0.25PF 0.25PF	500
C7 C8 C9	1-163-021-00 1-163-021-00 1-163-021-00	CERAMIC CHIP 0.01MF 50V CERAMIC CHIP 0.01MF 50V CERAMIC CHIP 0.01MF 50V	C53 C54 C55	1-163-085-00 1-163-021-00 1-163-021-00	CERAMIC CHIP 2PF CERAMIC CHIP 0.01MF CERAMIC CHIP 0.01MF	0.25PF 10% 10%	50V 50V 50V
C10 C11 C12	1-163-021-00 1-163-021-00 1-163-021-00	CERAMIC CHIP 0.01MF 50V CERAMIC CHIP 0.01MF 50V CERAMIC CHIP 0.01MF 50V	C56 C57 C58	1-163-021-00 1-163-077-00 1-163-021-00	CERAMIC CHIP 0.01MF CERAMIC CHIP 0.1MF CERAMIC CHIP 0.01MF	10% 10% 10%	50V 25V 50V
C13 C14 C15	1-163-021-00	CERAMIC CHIP 0.01MF 50V CERAMIC CHIP 0.01MF 10% 50V CERAMIC CHIP 0.047MF 10% 25V	C59 C60 C61	1-163-141-00	CERAMIC CHIP 0.001MF ELECT 22MF	10%	50V 50V 10V
C16 C17	1-163-141-00	CERAMIC CHIP 0.047MF 10% 25V CERAMIC CHIP 0.001MF 10% 50V	C62	1-163-021-00	CERAMIC CHIP 0.01MF (US, Canadian, E)	10%	50V 888
C18		CERAMIC CHIP 0.01MF 10% 50V	C63		CERAMIC CHIP	2PF 0.2	25PF 50V
C19 C20 C21	1-163-021-00 1-123-646-00 1-163-095-00	ELECT 33MF 20% 6.3V			CERAMIC CHIP		6.3V
C22 C23 C24	1-163-081-00	CERAMIC CHIP 1.5PF 0.25PF 50V CERAMIC CHIP 0.22MF 10% 25V CERAMIC CHIP 0.47MF 16V	C65 C66	1-163-141-00 1-163-086-00	CERAMIC CHIP 0.001MF CERAMIC CHIP 3PF	0.25PF	50V F 50V
C25 C26 C27	1-163-077-00 1-163-033-00	CERAMIC CHIP 0.1MF 10% 25V CERAMIC CHIP 0.022MF 10% 25V CERAMIC CHIP 0.022MF 10% 25V	C67 C68 C69	1-163-093-00 1-163-141-00		1-215-0	50V 50V 50V
C28	1-123-644-61	ELECT 22MF 20% 10V	C70 C71 C72	1-163-021-00	CERAMIC CHIP 27PF CERAMIC CHIP 0.01MF CERAMIC CHIP 0.01MF	10%	50V 50V 50V
	HP 10K 5% OZ AND LATER) HP 150K 5%						

In UA.	: µА	ase, U	: μ, f	or exam μPA,	ple: ^{OM} JAS UPC: μPC,	
						R2 R3
Part	82	RICAL F		METAL		

	ELECTRIC	AL PARTS
Ref.No.	Part No.	Description Description
C29	1-163-133-00	CERAMIC CHIP 470PF 10% 50V
C30	1-163-033-00	CERAMIC CHIP 0'.022MF 10% 25V
C31	1-163-109-00	CERAMIC CHIP 47PF 5% 50V
C32	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C33	1-123-822-00	ELECT 47MF 20% 10V
C34	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C35	1-163-033-00	CERAMIC CHIP 0.022MF 10% 25V
C36	1-163-033-00	CERAMIC CHIP 0.022MF 10% 25V
C37	1-163-141-00	CERAMIC CHIP 0.001MF 10% 50V
C38	1-163-101-00	CERAMIC CHIP 22PF 5% 50V
C39	1-163-117-00	CERAMIC CHIP 100PF 5% 50V
C40	1-163-117-00	CERAMIC CHIP 100PF 5% 50V
C41	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C42	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C43	1-163-085-00	CERAMIC CHIP 2PF 0.25PF 50V
C44	1-161-055-00	CERAMIC 0.022MF 20% 25V
C45	1-163-086-00	CERAMIC CHIP 3PF 0.25PF 50V
C46	1-163-085-00	CERAMIC CHIP 2PF 0.25PF 50V
C47	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C48	1-161-055-00	CERAMIC 0.022MF 20% 25V
C49	1-163-086-00	CERAMIC CHIP 3PF 0.25PF 50V
C50	1-163-086-00	CERAMIC CHIP 3PF 0.25PF 50V
C51	1-163-088-00	CERAMIC CHIP 5PF 0.25PF 50V
C52	1-163-083-00	CERAMIC CHIP 1PF 0.25PF 50V
C53	1-163-085-00	CERAMIC CHIP 2PF 0.25PF 50V
C54	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C55	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C56	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C57	1-163-077-00	CERAMIC CHIP 0.1MF 10% 25V
C58	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C59	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C60	1-163-141-00	CERAMIC CHIP 0.001MF 50V
C61	1-123-644-61	ELECT 22MF 20% 10V
C62	1-163-021-00	CERAMIC CHIP 0.01MF 10% 50V
C63	1-163-085-00	(US, Canadian, E)
	WO EVE THE WE	CERAMIC CHIP 2PF 0.25PF 50V
C63	1-163-086-00	(AEP,UK,FRANCE,AUS)CERAMIC CHIP 3PF 0.25PF 50V
C64	1-124-224-00	AND CALL CALL OF THE CALL CALL
C65	1-163-141-00	FOU.
C66	1-163-086-00	
C67	1-163-141-00	CERAMIC CHIP 0.001MF 10% 50V
C68	1-163-093-00	
C69	1-163-141-00	
C70	1-163-103-00	CERAMIC CHIP 27PF 5% 50V
C71	1-163-021-00	
C72	1-163-021-00	
	MADES IN THE	AL GIRL HATEN OUTDINGTO 150

	ELECTRIC	AL PARTS			
Ref.No.	Part No.	Description	art No.		
C73	1-163-021-00	CERAMIC CHIP			
C74	1-123-617-00	ELECT TOO	10MF		
C75	1-124-245-00	ELECT	4.7ME_014_	20%	25V
C76	1-123-617-00	ELECT JIOO	10MF 5-304-		
C77	1-163-063-00	CERAMIC CHIP			
C78	1-124-638-11	ELECT	22MF - 019-	20%	6.3
C79	1-123-311-00	(UP TO SERIA	L No.7901)		
	CE, AUS) COIL		1000MF		101
C79	1-124-142-00	(SERIAL No.79	470MF	20%	100
		MEIDER STALL	II-E4/OHES	200	101
C80	1-163-077-00	CERAMIC CHIP		10%	25V
C81	1-163-075-00	CERAMIC CHIP		10%	25V
C82	1-124-140-00	ELECT	220MF	20%	100
C83	1-124-245-00	ELECT	4.7MF	20%	25V
C84	1-124-465-00	ELECT	0.47MF	20%	501
C85	1-163-035-00	CERAMIC CHIP	0.047MF	10%	25V
C86	1-163-077-00	CERAMIC CHIP	0.1MF	10%	25V
C87	1-163-021-00	CERAMIC CHIP	0.01MF	10%	500
C88	1-163-021-00	CERAMIC CHIP	0.01MF	10%	501
C89	1-124-638-11	ELECT	22MF - 108-	20%	6.3V
C90	1-163-036-00	ELECT	0.068MF	20%	507
C91	1-163-035-00	CERAMIC CHIP	0.047MF	10%	25V
C92	1-123-617-00	ELECT	10MF 25T-	20%	16V
C93	1-124-139-00	ELECT	100MF	20%	100
C94	1-163-141-00	CERAMIC CHIP		10%	50V
C95	1-163-038-00	CERAMIC CHIP	0.1MF		25V
C96	1-163-109-00	CERAMIC CHIP		5%	50V
C97	1-163-021-00	CERAMIC CHIP		10%	50V
C98	1 124 422 00	ELECT	47MF - 25T	20%	44
C100	1-124-432-00 1-130-768-00	FILM	0.1MF	10%	63V
C101	1-124-436-00	ELECT	3.3MF	20%	25V
		TRANSLESS OF			0
C102	1-126-166-21	ELECT	2200MF	8 200	5.5V
C103 C104	1-123-617-00 1-163-021-00	CERAMIC CHIP	10MF 0.01MF	20%	16V 50V
0104	1-103-021-00	CERAPIC CHIP	0.01111	10%	301
C105	1-163-141-00	CERAMIC CHIP		10%	50V
C106	1-135-076-00	TANTAL. CHIP		20%	25V
C107	1-163-021-00	CERAMIC CHIP	0.01MF	10%	501
C108	1-124-139-00	ELECT	100MF	20%	100
C109	1-163-117-00	CERAMIC CHIP		5%	501
C110	1-163-035-00	CERAMIC CHIP	0.047MF	10%	25V
C111	1-163-035-00	CERAMIC CHIP	0.047MF	10%	25V
C112	1-163-035-00	CERAMIC CHIP		10%	257
C113	1-163-033-00	CERAMIC CHIP		10%	25 V

8	1-124-638-11	ELECTOROIM	22MF - 0 11-1	20%	6.3V	
9	1-123-311-00					
9	ICE AUS) COIL		1000MF		104	
IN)	1-124-142-00	(SERIAL No.79		20%	107	
10	1-163-077-00	CERAMIC CHIP	0.1MF	10%	25V	
1	1-163-075-00	CERAMIC CHIP		10%	25V	
2	1-124-140-00	ELECT	220MF	20%	107	
13	1-124-245-00	ELECT	4.7MF	20%	25V	275
14	1-124-465-00	ELECT	0.47MF	20%	50 V	
15	1-163-035-00	CERAMIC CHIP	0.04/MF	10%	25V	
36	1-163-077-00	CERAMIC CHIP	0.1MF	10%	25 V	
37	1-163-021-00	CERAMIC CHIP		10%	50V	
88	1-163-021-00	CERAMIC CHIP		10%	50V	
39	1-124-638-11	ELECT	22MF - 108-1	20%	6.3V	
10 101	1-163-036-00	ELECT	0.068MF	20%	50V	
1 arup	1-163-035-00	CERAMIC CHIP	0.047MF	10%	25V	
2	1-123-617-00	ELECT	10MF 251-8	20%	16V	
13	1-124-139-00	ELECT	100MF	20%	100	158123
94	1-163-141-00	CERAMIC CHIP		10%	50V	
95	1-163-038-00	CERAMIC CHIP	0.1MF		25V	
96	1-163-109-00	CERAMIC CHIP		5%	50V	
97	1-163-021-00	CERAMIC CHIP	0.01MF	10%	501	
98	1-124-432-00	ELECT	47MF -951-8	20%	44	
00	1-130-768-00	FILM	0.1MF	10%	63V	
01	1-124-436-00	ELECT	3.3MF	20%	25V	31862
02	1-126-166-21	ELECT	2200MF		5.5V	
03	1-123-617-00	ELECT	10MF	20%	167	
04	1-163-021-00	CERAMIC CHIP	0.01MF	10%	501	
105	1-163-141-00	CERAMIC CHIP	0.001MF	10%	50V	
06	1-135-076-00	TANTAL. CHIP		20%	25V	
107	1-163-021-00	CERAMIC CHIP		10%	501	
108	1-124-139-00	ELECT	100MF	20%	107	
109	1-163-117-00	CERAMIC CHIP	100PF	5%	50 V	
110	1-163-035-00	CERAMIC CHIP	0.047MF	10%	25V	
111	1-163-035-00	CERAMIC CHIP	0.047MF	10%	25 V	
12	1-163-035-00	CERAMIC CHIP	0.047MF	10%	25 V	
113	1-163-033-00	CERAMIC CHIP	0.022MF	10%	251	
114	1-163-021-00	CERAMIC CHIP	0.01MF	10%	500	
115	1-163-119-00	CERAMIC CHIP		5%	501	
116	1-123-311-00	(UP TO SERIA			100	
		gotzizwagt	1-729-100-66		101	
117	1-123-617-00	(SERIAL No.7	902 AND LATE	R) as	164	
201	1-163-038-00	A LEWNSTELLOW S	0.1MF	20%	16V 25V	
				127		
202		CERAMIC CHIP				
203		CERAMIC CHIP				
204	1-135-103-00	TANTAL. CHIP	3.3MF 3-729-100-66	20%	44	
205	1-163-141-00	CERAMIC CHIP				
206		CERAMIC CHIP				
207		CERAMIC CHIP	330PF			
209	1-163-141-00	CERAMIC CHIP	0.001MF		507	
210	1-163-093-00	(UP TO SERIA				
			IC CHIP 10PF		500	0
210	1-163-097-00	(SERIAL No.7	902 AND LATE			
		***OLKAN	TO OHIT TOFF			1

ELECTRICAL PARTS AS JACIATOLIS

		EI	LECIKICA	AL PART	2143 744					
Ref	.No.	Part No	0.	Descri	ption					
C	211	1-163-	100-00			No.790			025	
С	211	1-163-1	106-00	(SERIAL	L No.79	C CHIP O2 AND C CHIP	LATER)		028	0.0
C	212	1-102-	114-00			No.790			080	·01
C	212	1-163-	133-00	(SERIA		02 AND C CHIP				
C	214	1-163-0	077-00	CERAMI	C CHIP	0.1MF	6-113-	0%	AE 2	51
	215 217	1-135-1 1-163-0		TANTAL	C CHIP	3.3MF		20%		V
	218	1-163-			C CHIP					VO
	219	1-130-		FILM	C CHIP	0.56MF		10%		3 V
	221	1-163-			C CHIP			10%	MC.UE	OV
C	222	1-162-	611-00		nadian,	E) MIC CHI	P 1MF		140	251
C	222	1-162-	638-11	(AEP, U	K, FRANC					6V
C	223	1-163-		152837	CERAN	002 AND	P 0.001	LMF	10%	50V
C	224	1-163-	141-00	(SERIA	. CERAN	OO2 AND	P 0.001	lMF	10%	50V
0	225	1-163-	141-00			OO2 AND			10%	50V
0	226	1-163-	141-00	(SERIA	L No.79	OO2 AND	LATER)	10%	
C	F-A1	1-527-	982-00	FILTER	, CERAN					
-	F-F1	1-567-		FILTER	, CERAN	AIC DI			ICS	
	F-F2 F-F3	1-567- 1-567-			, CERAN					
(NI ·	*1-562-	990-11	SOCKET	, CONNE	ECTOR 1				
	T1 T2	1-141-	229-00	CAP, T	RIMMER				1C20	
(CT201	1-141-	311-11	(UP TO		No.79 P, VAR,			CHIE)
-	01	8_719_	101-23	DIODE	155123					
[)2		123-79	DIODE	1SS279 1SS279					
[04	8-719-	123-79	DIODE	155279					
	05 06		123-79		1SS279 1SS279					
			123-79		155279					
	07 08		123-79	T. T	1SS279					
1	09	8-719-	123-79	DIODE	155279					
	010		-123-79 -123-79		1SS279 1SS279					
	D11 D12		-123-79		155279					
	013	8-719-	-123-79	DIODE	155279					
	D14 D15		-123-79 -123-79		1SS279 1SS279					
	D16 D17 D18		-123-79 -123-79 -104-26	DIODE	1SS279 1SS279 1S2837		1-459-1 1-410-1 1-402-1			
					155279					
	D19 D20	8-719- 8-719-	-100-05		155279					
	D21	8-719-	-300-00	DIODE	1T33					
	D22 D23 D24	8-713-	-300-00 -220-00 -220-00		1T33 1T32-2 1T32-2					

	ELECTRIC	AL PARTS AS JASISTOSJE	ELECTRI	CAL PARTS MANIATOME		ELECTRICAL PARTS	ELECTRICAL PARTS
Ref.No.	Part No.	Description LOW 2769 LOW-769	Ref.No. Part No.	Description ON 2189 ON. 759	Ref.No.	Part No. Description	Ref.No. Part No. Description
D25 V D26 D28	8-719-100-05	DIODE 152837 DIODE 152837	L21 1-406-234-11	MICRO INDUCTOR 100UH COIL (OSC) MICRO INDUCTOR 2.7UH	R1 R2 R3	1-216-049-00 METAL CHIP 1K. 5% 1/10W 1-216-041-00 METAL CHIP 470 5% 1/10W 1-216-121-00 METAL CHIP 1M 5% 1/10W	R53 1-216-063-00 (UP TO SERIAL NO.7901)METAL CHIP 3.9K 5% 1/10W R54 1-216-049-00 (UP TO SERIAL NO.7901)METAL CHIP 1K 5% 1/10W
D29 D30 D31	8-719-100-05	D10DE 1S2837 D10DE 1S2837	L24 1-410-498-11 L25 1-410-334-11	COIL (OSC) MICRO INDUCTOR 1.2UH MICRO INDUCTOR 100UH	R4 R5 R6	1-216-025-00 METAL CHIP 100 5% 1/10W 1-216-045-00 METAL CHIP 680 5% 1/10W 1-216-025-00 METAL CHIP 100 5% 1/10W	R55 1-216-037-00 (UP TO SERIAL No.7901) METAL CHIP 330 5% 1/10W R56 1-216-049-00 (UP TO SERIAL No.7901)
D33 D34	8-719-106-98 8-719-101-23 8-719-100-05	DIODE RO16M-B DIODE 1SS123 DIODE 1S2837	L26 1-459-741-11 L27 1-459-733-11	(US,Canadian,E)COIL (WITH CORE) (AEP,UK,FRANCE,AUS)COIL (WITH CORE) (US,Canadian,E)COIL (WITH CORE) (AEP,UK,FRANCE,AUS)COIL (WITH CORE)	R7 R8 R9 R10	1-216-025-00 METAL CHIP 100 5% 1/10W 1-216-025-00 METAL CHIP 100 5% 1/10W 1-216-061-00 METAL CHIP 3.3K 5% 1/10W 1-216-097-00 METAL CHIP 100K 5% 1/10W	R57 1-216-059-00 METAL CHIP 2.7K 5% 1/10W R58 1-216-055-00 METAL CHIP 1.8K 5% 1/10W R59 1-216-073-00 METAL CHIP 10K 5% 1/10W
D36 D37 D38 D39	8-719-123-79	DIODE 1SS279 DIODE 1SS279 (UP TO SERIAL No.7901)DIODE 1SS279	L28 1-459-734-11 L28 1-459-742-11	(US,Canadian,E)COIL (WITH CORE) (AEP,UK,FRANCE,AUS)COIL (WITH CORE) COIL, AIR-CORE	R11	1-216-059-00 (FOR Q2:2SK508-K51) METAL CHIP 2.7K 5% 1/10W 1-216-065-00 (FOR Q2:2SK508-K52) METAL CHIP 4.7K 5% 1/10W	R60 1-216-067-00 METAL CHIP 5.6K 5% 1/10W R61 1-216-073-00 METAL CHIP 10K 5% 1/10W R62 1-216-059-00 METAL CHIP 2.7K 5% 1/10W
D41 D42 D201	8-713-220-00 8-713-220-00	(US)DIODE 1T32-2 (US)DIODE 1T32-2 (US)DIODE 1T32-2 DIODE 1S2837	L30 1-410-336-11 L31 1-410-208-41 L201 1-410-210-21	MICRÓ INDUCTOR 220UH INDUCTOR CHIP 22UH INDUCTOR CHIP 33UH	R12 R13 R14	1-216-079-00 METAL CHIP 18K 5% 1/10W 1-216-025-00 METAL CHIP 100 5% 1/10W 1-216-053-00 METAL CHIP 1.5K 5% 1/10W	R63 1-216-069-00 METAL CHIP 6.8K 5% 1/10W R64 1-216-097-00 METAL CHIP 100K 5% 1/10W R65 1-216-057-00 METAL CHIP 2.2K 5% 1/10W
D202 D203 D204	8-719-100-05 8-719-100-05 8-719-100-05	DIODE 152837 DIODE 152837 DIODE 152837	LCD1 1-807-778-11	(US,Canadian,E)DISPLAY PANEL, LIQUID CRYSTAL (AEP,UK,FRANCE,AUS)DISPLAY PANEL, LIQUID CRYSTAL (AEP(ITALY))	R15 R16 R17	1-216-057-00 METAL CHIP 2.2K 5% 1/10W 1-216-025-00 METAL CHIP 100 5% 1/10W 1-216-057-00 METAL CHIP 2.2K 5% 1/10W 1-216-049-00 METAL CHIP 1K 5% 1/10W	R66 1-216-049-00 METAL CHIP 1K 5% 1/10W R67 1-216-067-00 METAL CHIP 5.6K 5% 1/10W R68 1-216-059-00 METAL CHIP 2.7K 5% 1/10W
D205 D206 D207	8-719-100-05 8-719-912-43	DIODE 1S2837 DIODE SLP178B (SERIAL No.7902 AND LATER)DIODE 1SS123	Q1 8-729-123-86 Q2 8-729-116-64	DISPLAY PANEL,LIQUID CRYSTAL TRANSISTOR 2SK238-K16 TRANSISTOR 2SK508-K51 TRANSISTOR 2SK94-X2	R19 R20 R21	1-216-085-00 METAL CHIP 33K 5% 1/10W 1-216-053-00 METAL CHIP 1.5K 5% 1/10W 1-216-049-00 METAL CHIP 1K 5% 1/10W	R69 1-216-097-00 METAL CHIP 100K 5% 1/10W R70 1-216-017-00 METAL CHIP 47 5% 1/10W R71 1-216-049-00 METAL CHIP 1K 5% 1/10W R72 1-216-091-00 METAL CHIP 56K 5% 1/10W
IC1 IC2 IC3	8-759-205-09 8-759-012-60	IC TC74HC138F IC TC74HC138F IC MC74HC595F	Q5 8-729-123-86	TRANSISTOR 2SC2223 TRANSISTOR 2SK23B-K16A TRANSISTOR 2SK94-X2	R22 R23 R24 R25	1-216-025-00 METAL CHIP 100 5% 1/10W 1-216-115-00 METAL CHIP 560K 5% 1/10W 1-216-081-00 METAL CHIP 22K 5% 1/10W 1-216-049-00 METAL CHIP 1K 5% 1/10W	R73 1-216-095-00 METAL CHIP 82K 5% 1/10W R74 1-216-089-00 METAL CHIP 47K 5% 1/10W R75 1-216-049-00 METAL CHIP 1K 5% 1/10W R76 1-216-025-00 METAL CHIP 100 5% 1/10W
IC4 IC5 IC6 IC7			Q8 8-729-102-06 Q9 8-729-109-42	TRANSISTOR 2SC2223 TRANSISTOR 2SC2223 TRANSISTOR 2SK94-X2	R26 R27 R28 R29	1-216-097-00 METAL CHIP 100K 5% 1/10W 1-216-043-00 METAL CHIP 560 5% 1/10W 1-216-023-00 METAL CHIP 82 5% 1/10W 1-216-081-00 METAL CHIP 22K 5% 1/10W	R77 1-216-025-00 METAL CHIP 100 5% 1/10W R78 1-216-025-00 METAL CHIP 100 5% 1/10W R79 1-216-296-00 METAL CHIP 0 5% 1/8W
	8-759-932-06 8-759-204-94		Q11 8-729-901-05 Q12 8-729-901-00	TRANSISTOR DTA124EK TRANSISTOR DTA124EK TRANSISTOR DTC124EK TRANSISTOR DTC124EK	R30 R31 R32	1-216-081-00 METAL CHIP 22K 5% 1/10W 1-216-083-00 METAL CHIP 27K 5% 1/10W 1-216-053-00 METAL CHIP 1.5K 5% 1/10W 1-216-033-00 METAL CHIP 220 5% 1/10W	R81 1-216-296-00 METAL CHIP 0 5% 1/8W R82 1-216-295-00 METAL CHIP 0 5% 1/10W R83 1-216-059-00 METAL CHIP 2.7K 5% 1/10W R84 1-216-045-00 METAL CHIP 680 5% 1/10W
	8-759-204-94 8-759-205-06	IC MC74HCOOF	Q14 8-729-901-05 Q15 8-729-901-00 Q16 8-729-123-86	TRANSISTOR DTA124EK TRANSISTOR DTC124EK TRANSISTOR 2SK238-K16	R33 R34 R35 R36	1-216-097-00 METAL CHIP 100K 5% 1/10W 1-216-049-00 METAL CHIP 1K 5% 1/10W 1-216-025-00 METAL CHIP 100 5% 1/10W 1-216-049-00 METAL CHIP 1K 5% 1/10W	R85 1-216-061-00 METAL CHIP 3.3K 5% 1/10W R86 1-216-033-00 METAL CHIP 220 5% 1/10W R87 1-216-033-00 METAL CHIP 220 5% 1/10W
J2 J3	1-507-562-00	JACK (RECORD) JACK, OUTSIDE POWER (DC IN 6V)		TRANSISTOR 2SC2223 TRANSISTOR DTC124EK	R37	1-216-001-00 (FOR Q16 OF 2SK238-K15)	R88 1-216-295-00 METAL CHIP 0 5% 1/10W R89 1-216-093-00 METAL CHIP 68K 5% 1/10W
L1 L2 L3	1-410-316-11 1-402-274-11 1-402-274-11		Vac sor Swrap o	(UP TO SERIAL No.7901)TRANSISTOR 2SC2712 (UP TO SERIAL No.7901)TRANSISTOR 2SC2712	R37	1-216-025-00 (FOR Q16 OF 25K238-K16) METAL CHIP 100 5% 1/10W	R90 1-216-025-00 METAL CHIP 100 5% 1/10W R91 1-216-049-00 METAL CHIP 1K 5% 1/10W R92 1-216-045-00 (UP TO SERIAL No.7901)
L4 L5 L6		COIL (ANT) COIL (WITH CORE)	Q22 8-729-100-66	TRANSISTOR 2SD596 TRANSISTOR 2SC1623 TRANSISTOR DTC124EK	R39 R40	1-216-025-00 METAL CHIP 100 5% 1/10W 1-216-097-00 METAL CHIP 100K 5% 1/10W 1-216-097-00 METAL CHIP 100K 5% 1/10W	R93 1-216-043-00 METAL CHIP 560 5% 1/10W R94 1-216-045-00 METAL CHIP 680 5% 1/10W R95 1-216-053-00 METAL CHIP 1.5K 5% 1/10W
L7 L8 L9	1-459-729-11 1-459-729-11	COIL (WITH CORE) COIL (WITH CORE) COIL (WITH CORE)	Q25 8-729-162-45 Q26 8-729-162-45	TRANSISTOR 2SC1623 TRANSISTOR 2SB624-BV5 TRANSISTOR 2SB624-BV5	R42 R43	1-216-091-00 METAL CHIP 56K 5% 1/10W 1-216-043-00 METAL CHIP 560 5% 1/10W 1-216-222-00 METAL CHIP 10K 5% 1/8W	R96 1-249-411-11 CARBON 330 5% 1/6W R98 1-216-065-00 (SERIAL No.7902 AND LATER) METAL CHIP 4.7K 5% 1/10W
L11 L12	1-459-730-11	COIL (WITH CORE)	Q28 8-729-100-66	TRANSISTOR 2SK94-X2 TRANSISTOR 2SC1623 TRANSISTOR 2SD596	R45 R46	1-216-057-00 METAL CHIP 2.2K 5% 1/10W 1-216-033-00 METAL CHIP 220 5% 1/10W	R99 1-216-061-00 (SERIAL No.7902 AND LATER) METAL CHIP 3.3K 5% 1/10W
L13 L14 L15	1-410-336-11	COIL (WITH CORE) MICRO INDUCTOR 220UH ANTENNA, FERRITE-ROD (LW/MW)	Q201 8-729-901-00 Q203 8-729-109-42	TRANSISTOR 2SC1623 TRANSISTOR DTC124EK TRANSISTOR 2SK94-X2	R47 R48 R50	1-216-091-00 METAL CHIP 56K 5% 1/10W 1-216-029-00 METAL CHIP 150 5% 1/10W 1-216-748-11 METAL CHIP 39K 5% 1/10W	R100 1-216-049-00 (SERIAL No.7902 AND LATER)METAL CHIP 1K 5% 1/10W R101 1-216-073-00 (SERIAL No.7902 AND LATER)
L16 L17 L18 L19	1-410-331-11 1-410-325-11	TRANSFORMER, HIGH FREQUENCY MICRO INDUCTOR 33UH MICRO INDUCTOR 5.6UH MICRO INDUCTOR 4.7UH	0.001MF 50V . No.7901) .C CHIP 10PF 5% 50V		R51 R52	1-216-049-00 METAL CHIP 1K 5% 1/10W 1-216-105-00 (UP TO SERIAL No.7901) METAL CHIP 220K 5% 1/10W	R102 1-216-101-00 (SERIAL NO.7902 AND LATER) METAL CHIP 150K 5% 1/10W

ELECTRICAL PARTS

ELECTRICAL PARTS EGILDE STRAT ALIENTE

Ref.No.	Part No.	Description	Ref.No. Part No. Description
R103	1-216-049-00	(SERIAL NO.7902 AND LATER)	R240 1-216-073-00 METAL CHIP 10K 5% 1/10W
R104	1-216-101-00	METAL CHIP 10K 5% 1/10W (SERIAL No.7902 AND LATER)METAL CHIP 150K 5% 1/10W	R241 1-216-073-00 METAL CHIP 10K 5% 1/10W R242 1-216-295-00 (UP TO SERIAL No.7901) METAL CHIP 0 5% 1/10W
R105	1-216-053-00	(SERIAL No.7902 AND LATER)METAL CHIP 1.5K 5% 1/10W	R243 1-216-073-00 METAL CHIP 10K 5% 1/10W
R106	1-216-033-00	(SERIAL No.7902 AND LATER)METAL CHIP 220 5% 1/10W	R244 1-216-073-00 METAL CHIP 10K 5% 1/10W R245 1-216-073-00 METAL CHIP 10K 5% 1/10W
R107	1-216-025-00	(SERIAL No.7902 AND LATER)	R247 1-216-073-00 METAL CHIP 10K 5% 1/10W
R110	1-216-097-00	METAL CHIP 100 5% 1/10W (UP TO SERIAL No.7901)METAL CHIP 100K 5% 1/10W	R248 1-216-037-00 (SERIAL NO.7902 AND LATER)METAL CHIP 330 5% 1/10W
R204 R205 R206	1-216-019-00 1-216-105-00 1-216-121-00	METAL CHIP 56 5% 1/10W METAL CHIP 220K 5% 1/10W METAL CHIP 1M 5% 1/10W	RV1 1-237-680-11 RES, VAR, SLIDE 50K (VOLUME) RV2 1-237-402-21 (SERIAL No.7902 AND LATER)RES, ADJ 1K (AGC)
R207 R208 R209	1-216-121-00 1-216-073-00 1-216-073-00	METAL CHIP 1M 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W	\$1 1-570-331-11 SWITCH, SLIDE (TONE) \$218 1-553-510-00 SWITCH, SLIDE (MW CH STER) \$219 1-553-510-00 SWITCH, SLIDE (MAIN POWER) \$220 1-553-510-00 SWITCH, SLIDE (ALARM)
R211 R212	1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W	SP1 1-502-631-00 SPEAKER
R213	1-216-073-00	METAL CHIP 10K 5% 1/10W	filly 30 T1 1 1-404-734-11 TRANSFORMER, IF 1 beau gried at linu off
R214 R215 R216	1-216-025-00 1-216-073-00 1-216-069-00	METAL CHIP 100 5% 1/10W METAL CHIP 10K 5% 1/10W	T2 1-404-362-41 TRANSFORMER, IF T3 1-448-915-11 TRANSFORMER, DC-DC CONVERTER
R217	1-216-049-00	METAL CHIP 6.8K 5% 1/10W	TH1 1-800-200-00 (UP TO SERIAL No.7901)THERMISTOR S-3K
R218 R219	1-216-049-00 1-216-067-00 1-216-121-00	METAL CHIP 1K 5% 1/10W METAL CHIP 5.6K 5% 1/10W METAL CHIP 1M 5% 1/10W	X1 1-567-840-11 VIBRATOR, CRYSTAL X201 1-567-769-21 VIBRATOR, CRYSTAL
R220 R222 R223	1-216-121-00 1-216-109-00 1-216-073-00	METAL CHIP 1M 5% 1/10W METAL CHIP 330K 5% 1/10W METAL CHIP 10K 5% 1/10W	XF1 1-567-823-11 FILTER, CRYSTAL as beginning to also well a content of the conte
R225 R226 R227	1-216-037-00 1-216-073-00 1-216-025-00	METAL CHIP 330 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 100 5% 1/10W	ACCESSORY & PACKING MATERIAL
		2/2011	for the state of t
R228	1-216-049-00	METAL CHIP 1K 5% 1/10W	Part No. Description
R228 R229 R230	1-216-049-00 1-216-073-00 1-216-073-00	METAL CHIP 1K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240
R229 R230 R231	1-216-073-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240
R229 R230	1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE
R229 R230 R231 R232 R233	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE(ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-619-00 BAG, POLYETHYLENE, STANDARD 3-701-623-00 BAG, POLYETHYLENE
R229 R230 R231 R232 R233 R234 R235	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-619-00 BAG, POLYETHYLENE, STANDARD 3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER
R229 R230 R231 R232 R233 R234 R235 R236	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W (UP TO SERIAL No.7901)METAL CHIP 10K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-619-00 BAG, POLYETHYLENE, STANDARD 3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER *3-703-264-10 (AEP,UK,FRANCE,AUS)LABEL, SERIAL NUMBER (B)
R229 R230 R231 R232 R233 R234 R235	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W (UP TO SERIAL No.7901)	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-619-00 BAG, POLYETHYLENE, STANDARD 3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER
R229 R230 R231 R232 R233 R234 R235 R236	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W (UP TO SERIAL No.7901)METAL CHIP 10K 5% 1/10W (SERIAL No.7902 AND LATER)METAL CHIP 5.6K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-619-00 BAG, POLYETHYLENE, STANDARD 3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER (AEP,UK,FRANCE,AUS)LABEL, SERIAL NUMBER (B) 3-898-304-01 (E)CARTON, INDIVIDUAL 3-898-365-01 GAEP,UK,FRANCE,AUS)CARTON, INDIVIDUAL 3-893-940-11 GUIDE, SHORT WAVE
R229 R230 R231 R232 R233 R234 R235 R236 R236	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (SERIAL NO.7902 AND LATER)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7902 AND LATER)METAL CHIP 10K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-619-00 BAG, POLYETHYLENE, STANDARD 3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER (AEP,UK,FRANCE,AUS)LABEL, SERIAL NUMBER (B) 3-898-304-01 (E)CARTON, INDIVIDUAL 3-898-365-01 (AEP,UK,FRANCE,AUS)CARTON, INDIVIDUAL 3-898-366-01 CUSHION 3-898-360-01 CASE, CARRYING 3-898-361-01 SHEFT, PROTECTION
R229 R230 R231 R232 R233 R234 R235 R236 R236 R237	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-067-00 1-216-067-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (SERIAL NO.7902 AND LATER)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W (SERIAL NO.7902 AND LATER)METAL CHIP 5.6K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-619-00 BAG, POLYETHYLENE 3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER *3-703-264-10 (AEP,UK,FRANCE,AUS)LABEL, SERIAL NUMBER (B) 3-898-304-01 (AEP,UK,FRANCE,AUS)CARTON, INDIVIDUAL 3-898-365-01 (AEP,UK,FRANCE,AUS)CARTON, INDIVIDUAL 3-898-366-01 CUSHION 3-898-360-01 CASE, CARRYING 3-898-361-01 SHEET, PROTECTION 3-990-102-11 (AEP,UK,FRANCE,AUS,E)
R229 R230 R231 R232 R233 R234 R235 R236 R236 R237	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (SERIAL NO.7902 AND LATER)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7902)METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-623-00 BAG, POLYETHYLENE *3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER (AEP,UK,FRANCE,AUS)LABEL, SERIAL NUMBER (B) 3-898-304-01 (E)CARTON, INDIVIDUAL 3-898-365-01 (AEP,UK,FRANCE,AUS)CARTON, INDIVIDUAL 3-898-306-01 CUSHION 3-898-306-01 CASE, CARRYING 3-898-361-01 SHEET, PROTECTION 3-990-102-11 (AEP,UK,FRANCE,AUS,E)
R229 R230 R231 R232 R233 R234 R235 R236 R236 R237	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-067-00 1-216-067-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (SERIAL NO.7902 AND LATER)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-623-00 BAG, POLYETHYLENE, STANDARD 3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER (AEP,UK,FRANCE,AUS)LABEL, SERIAL NUMBER (B) 3-898-304-01 (E)CARTON, INDIVIDUAL 3-898-365-01 (AEP,UK,FRANCE,AUS)CARTON, INDIVIDUAL 3-898-306-01 CUSHION 3-898-306-01 CASE, CARRYING 3-898-361-01 SHEET, PROTECTION 3-990-102-11 (AEP,UK,FRANCE,AUS,E)MANUAL, INSTRUCTION 3-990-102-21 (US,Canadian,E)MANUAL, INSTRUCTION
R229 R230 R231 R232 R233 R234 R235 R236 R236 R237	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (SERIAL NO.7902 AND LATER)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7902)METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (SERIAL NO.7902 AND LATER)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-619-00 BAG, POLYETHYLENE, STANDARD 3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER (AEP,UK,FRANCE,AUS)LABEL, SERIAL NUMBER (B) 3-898-304-01 (AEP,UK,FRANCE,AUS)CARTON, INDIVIDUAL 3-898-365-01 (AEP,UK,FRANCE,AUS)CARTON, INDIVIDUAL 3-898-306-01 CASE, CARRYING 3-898-306-01 CASE, CARRYING 3-898-361-01 SHEET, PROTECTION 3-990-102-21 (AEP,UK,FRANCE,AUS,E)MANUAL, INSTRUCTION 3-990-102-21 (AEP)MANUAL, INSTRUCTION 3-990-102-21 (AEP)MANUAL, INSTRUCTION 3-990-102-21 (AEP)MANUAL, INSTRUCTION (E,Saudi Arabia,MIDDLE EAST) MANUAL, INSTRUCTION (E,Saudi Arabia,MIDDLE EAST)
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R229 R230 R231 R232 R233 R234 R235 R236 R236 R237 R237 R237	1-216-073-00 1-216-073-00 1-216-073-00 1-216-061-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-067-00 1-216-067-00 1-216-073-00 1-216-073-00 1-216-073-00 1-216-073-00	METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 3.3K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7902 AND LATER)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 10K 5% 1/10W (UP TO SERIAL NO.7902)METAL CHIP 5.6K 5% 1/10W (UP TO SERIAL NO.7901)METAL CHIP 5.6K 5% 1/10W	1-463-659-11 (E)ADAPTOR, AC:AC-240 1-506-409-00 (E2)ADAPTOR, CONVERSION 1-504-059-11 MAGNETIC EARPHONE (ME-20H) 3-701-616-00 BAG, POLYETHYLENE 3-701-619-00 BAG, POLYETHYLENE, STANDARD 3-701-623-00 BAG, POLYETHYLENE *3-701-999-00 (US,Canadian,E)LABEL, SERIAL NUMBER (AEP,UK,FRANCE, AUS)LABEL, SERIAL NUMBER (B) 3-898-304-01 (AEP,UK,FRANCE, AUS)CARTON, INDIVIDUAL 3-898-365-01 (AEP,UK,FRANCE, AUS)CARTON, INDIVIDUAL 3-898-306-01 CASE, CARRYING 3-898-306-01 CASE, CARRYING 3-898-361-01 (AEP,UK,FRANCE, AUS,E)MANUAL, INSTRUCTION 3-990-102-21 (AEP)MANUAL, INSTRUCTION 3-990-102-52 (E,Saudi Arabia, MIDDLE EAST)MANUAL, INSTRUCTION 3-990-102-31 (Canadian)MANUAL, INSTRUCTION 3-990-102-31 (Canadian)MANUAL, INSTRUCTION 3-990-102-31 (Canadian)MANUAL, INSTRUCTION

Sony Corporation

ICF-7600DA/7700

TROUBLESHOOTING GUIDE 27849 MAISTORNS

Should any problem occur with the unit, make the following simple checks to determine whether or not servicing is required. If the problem persists after you have made these checks, consult the nearest Sony dealer for further information.

The power is not turned on even if you press the ON/OFF button.

- · Weak batteries.
- Incorrect polarity of batteries.
- The MAIN POWER switch is set to OFF.
- The KEY PROTECT is set.
- The AC power adaptor, the battery case or the car battery cord is disconnected.
- Battery operation is attempted while the AC power adaptor, the battery case or the car battery cord is connected to the unit, but not to a wall outlet or cigarette lighter socket.

Each function does not work even if operating the unit.

• The KEY PROTECT is set.

Display is dim.

- Weak batteries.
- The unit is being used in extremely high temperatures or in a place with excessive moisture.

Sound is not heard at all.

- The VOLUME control is turned down completely.
- The earphone is plugged in.

Very weak or interrupted sound, or unsatisfactory reception.

- Weak batteries.
- Tuning or antenna adjustment is not correct.
- Weak signal. → When in a vehicle or in a building, listen near a window.
- Figures on the frequency display differ a little from the acted station frequency. → Tune more precisely. (See page 12.)

The preset station cannot be received when preset tuning.

- Incorrect preset station button is pressed.
- The preset station is erased. → Preset the station again.

The radio does not turned on at the alarm-setting time.

- The STANDBY button has not been pressed.
- The alarm-setting time is erased. ⇒ Set the alarm again.
- The MAIN POWER switch is set to OFF.

Sony Corporation

Audio Group

English 87H0298-1 Printed in Japan © 1987. 8

ICF-7600DA/7700

SONY. SERVICE MANUAL

AEP Model

UK Model

E Model

SUPPLEMENT-1

AUS Model

File this supplement -1 with the service manual.

US Model

- Subject:
- Main board change Italian model addition

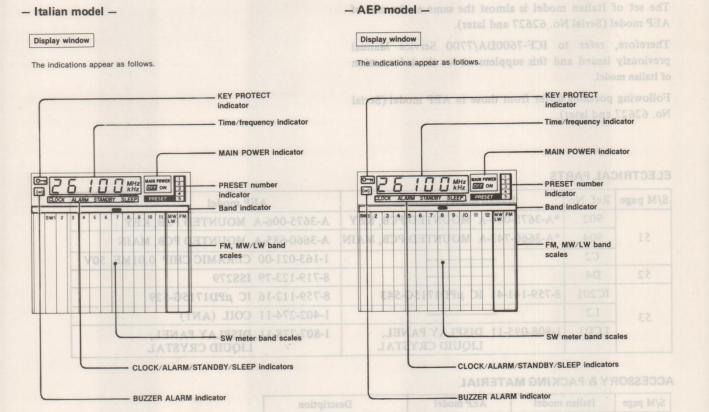
- Canadian Model
- Revise of EXPLODED VIEWS

ICF-7700

Distinction between AEP model and Italian model a part of indication of display window differs between AEP and Italian model.

AEP model 8: SW1 to 12 38 SHP 38 GLAZE CHIP 38 GLAZE G

Italian model: SW1 to 11



1. Main board has been changed.

Applicable Serial No.: 62627 and later.

Because of this, schematic diagram has partly changed.

NOTE:

• Following parts differ with former type and new type.

CAPACITORS: MF: μF, PF: μμF.

Distinction

Ref. No.	Former from Serial No. 7902 to 62626	New Serial No. 62627 and later	Remarks
C20	ELECT 33MF 6.3V 1	-126-205-11 ELECT 47MF 6.3V	changed
C44	CERAMIC 0.022MF 25V 1	-161-051-00 CERAMIC 0.01MF 25V	changed
C48	CERAMIC 0.022MF 25V 1	-161-051-00 CERAMIC 0.01MF 25V	changed
C83	ELECT 4.7MF 25V 1	-126-198-11 ELECT 4.7MF 35V	changed
C84	ELECT 0.47MF 50V 1	-135-083-00 TANTALUM CHIP 0.47MF 25V	changed
C106	TANTALUM CHIP 1MF 25V 1	-162-638-11 CERAMIC CHIP 1MF 16V	changed
C211	CERAMIC CHIP 36PF 50V 1	-163-102-00 CERAMIC CHIP 24PF 50V	changed
R81	METAL CHIP 0Ω 1/8W	the language of the second like I have below 95	deleted
R88	METAL CHIP 0Ω 1/10W	differs between A FP and Italian model	deleted
R99	METAL CHIP 3.3kΩ 1/10W 1	-216-060-00 METAL GLAZE CHIP 3kΩ 1/10W	changed

2. Italian model addition

The set of Italian model is almost the same as the one of AEP model (Serial No. 62627 and later).

Therefore, refer to ICF-7600DA/7700 Service Manual previously issued and this supplement for the information of Italian model.

Following portions differ from those in AEP model (Serial No. 62627 and later).

ELECTRICAL PARTS

S/M page	Ref. No.	Italian model	AEP model
	902	*A-3675-008-A MOUNTED PCB, KEY	A-3675-006-A MOUNTED PCB, KEY
51	904	*A-3660-743-A MOUNTED PCB, MAIN	A-3660-685-A MOUNTED PCB, MAIN
	C2	Clara	1-163-021-00 CERAMIC CHIP 0.01MF 50V
52	D4		8-719-123-79 ISS279
	IC201	8-759-141-41 IC μPD1715G-543	8-759-112-16 IC μPD1715G-529
53	L2		1-402-274-11 COIL (ANT)
les	LCD1	1-808-095-11 DISPLAY PANEL, LIQUID CRYSTAL	1-807-778-11 DISPLAY PANEL, LIQUID CRYSTAL

ACCESSORY & PACKING MATERIAL

S/M page	Italian model	AEP model	Description
	3-898-393-01	3-898-365-01	CARTON, INDIVIDUAL
55		3-990-102-11	MANUAL, INSTRUCTION
	3-990-102-61	3-990-102-41	MANUAL, INSTRUCTION

(): TERMINAL NR. OF LCD

Service manual page 1.
 SPECIFICATIONS

Frequency rang FM: US, Canadian model: 76.0-108.0 MHz

AEP, Italian, UK, E, AUS model: 87.5-108.0 MHz

MW: Except for Italian model: 530-1,700 kHz Italian model : 531-1,602 kHz

LW: Except for Italian model: 150-285 kHz

Italian model : 147-285 kHz

SW: Except for Italian model: 90-11 meter SW broadcast bands
Italian model : 75-11 meter SW broadcast bands

Service manual page 5.

1-2. OUTLINE OF THE C-MOS DIGITAL-TUNING SYSTEM IC201, µPD1715G-529 ((µPD1715G-543)).

1-2-1. OUTLINE OF THE STATION-SELECTION

1) Receiving-frequency Coverages:

The following table shows the frequency coverages the $\mu PD1715G-529$ (($\mu PD1715G-543$)) can receive.

BAND	METER BAND	FREQUENCY COVERAGE	CHANNEL SEPARATION	NUMBER OF CHANNEL	NOMINAL SPACING	INTERMEDIATE FREQUENCY
LW		150kHz ~ 285kHz ((147kHz~285kHz))	3kHz	46ch ((47ch))	3kHz	10.71MHz
MW1	728	531kHz ~ 1,602kHz	3kHz	359ch	3kHz	10.71MHz
MW2		530kHz~ 1,700kHz	5kHz	235ch	5kHz	10.71MHz
P Y	90m *1	3,050kHz~ 3,565kHz	5kHz	104ch	5kHz	10.71MHz
	75m	3,700kHz~ 4,215kHz ((3,850kHz~4,365kHz))	5kHz	104ch	5kHz	10.71MHz
到了	60m	4,650kHz ~ 5,165kHz	5kHz	104ch	5kHz	10.71MHz
	49m	5,800kHz~ 6,315kHz	5kHz	104ch	5kHz	10.71MHz
	41m	6,950kHz~ 7,465kHz	5kHz	104ch	5kHz	10.71MHz
SW	31m	9,375kHz~10,010kHz	5kHz	128ch	5kHz	10.71MHz
SW	25m	11,525kHz~12,160kHz	5kHz	128ch	5kHz	10.71MHz
	21m	13,375kHz~14,010kHz	5kHz	128ch	5kHz	10.71MHz
	19m	14,975kHz~15,610kHz	5kHz	128ch	5kHz	10.71MHz
31034	16m	17,475kHz~18,110kHz	5kHz	128ch	5kHz	10.71MHz
	13m	21,325kHz~21,960kHz	5kHz	128ch	5kHz	10.71MHz
	11m	25,475kHz~26,100kHz	5kHz	126ch	5kHz	10.71MHz
FM1	100	87.50MHz~108.00MHz	50 kHz	411ch	50kHz	10.7MHz
FM2		76.00MHz~108.00MHz	50kHz	641ch	50kHz	10.7MHz

NOTE: LW and MW1 (or MW2) bands are taken into a single band in the µPD1715G-529 ((µPD1715G-543)).

(()): Italian model

*1 : Italian model has no 90m band.

Service manual page 9.

PIN Nr.	SYMBOL	NAMING		DESCRIPTION				
(cont'd)			BAND (METERBAND)	BAND-OUTPUT CODE (BINARY) MSB←→LSB	SWH	SWL		
	+	TY	LW/MW1/MW2	0 0 1 0	Low	High		
			SW (90m)*1 SW (75m) SW (60m) SW (49m) SW (41m) SW (31m) SW (25m) SW (21m) SW (19m) SW (16m) SW (13m) SW (11m) FM1/FM2 NOTE: Also refer to 1-	0 0 1 1 0 0 0 0 1 0 1 0 1 0 1 0 0 1 1 0 0 0 0 1 1 1 1 1 1 1 1 1 0 0 0 0 1 1 1 1 1 1 1 1 1 1 0 0 0 1	Low Low Low Low High High High High High Low Low Low	High High High High Low		

*1: Italian model has no 90m band.

Service manual page 11.

JP (□) DOWN (□)	Shortwave Meterband Change	The shortwave receiving spectrum is divided into 12 (twelve) *1 broadcast bands. Each band up to 41-meter band is further divided into 104 channels, and each band up to 13-meter band is further divided into 128 channels. The 11-meter band is divided into 126 channel. a) When the "UP" key is pressed while receiving a shortwave band, the receiving band changes to the next higher band successively. When the SW12 (11-meter) *2 band is reached, however, the band changes to the lowest band, i.e., SW1 (90-meter) *3 band. The band indicator on the LCD moves appropriately each time the band is changed. b) When the "DOWN" key is pressed while receiving a shortwave band, the receiving band changes to the next adjacent lower band successively. When the SW1 (90-meter) *3 band is reached, however, the band changes to the highest band, i.e., SW12 (11-meter) *2 band. The band indicator on the LCD moves appropriately each time the band is changed. c) When the "UP" or "DOWN" key is kept depressed approximately for more than 500 msec, the band changes up or down to the adjacent band at an interval of approximately 250 msec until the lowest or the highest band is reached at which the band changing ceases. d) When the SW band is changed up and down, the receiver receives the same channel number allocated for this receiver as the last channel of the last band. When the band is changed from SW6 (31-meter) *4 to SW5 (41-meter) *5 and the receiving channel has been higher than the channel 104, however, the receiver receives the channel 104, i.e., the highest channel of these lower shortwave band group of this receiver. When the band is changed from SW12 (11-meter) *2 to SW1 (90-meter) *3
		band and the receiving channel has been higher than the channel 102, the receiver receives the channel 102.

*1-5 are as follows in Italian model.

*1: 11 (eleven)

*2: SW11 (11-meter)

*3: SW1 (75-meter)

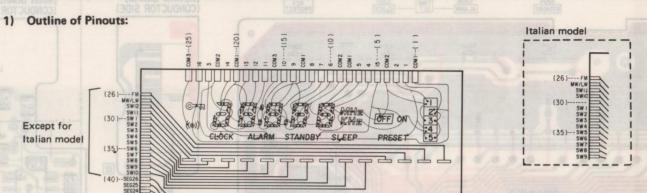
*4: SW5 (31-meter)

*5: SW4 (41-meter)

Service manual page 18.

1-4. ON THE NEW LCD PANEL, LCD1

This radio uses a new liquid-crystal display panel incorporating dial pointer itself, band indicator, ordinal frequency and clock indicators, etc. The outline of this LCD is as follows.



Service manual page 19.

CLOCK-FREQUENCY ADJUSTMENT

Procedure:

- 1. Adjust CT201 so that the reading on the frequency counter becomes in 10.860000 MHz (10.710 MHz + 0.150 MHz).
- 2. Hit the FM button. The band should become in FM and the frequency on the LCD panel of the set should be 76.00 MHz (US, Canadian model) or 87.5 MHz (AEP, Italian, UK, E, AUS model).
- 3. Hit the SW button. The band should become in the SW1 and the frequency on the LCD panel of the set should be 3,050 kHz (Except for Italian model) or 3.850 kHz (Italian model).

Service manual page 23.

SW TRACKING ADJUSTMENT

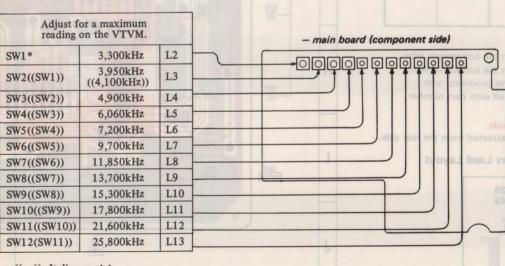
Procedure and Adjustment Location:

1. Check and verify the VCO voltages on the LPF line at both the lowest and highest frequency points as shown. See page 20 for the LPF line.

LOW ENDS	HIGH ENDS	
VCO(L): 150kHz VCO(H): 9,375kHz	VCO(L): 7,465kHz VCO(H): 21,960 kHz	
1.50~2.00V	12. 5~15. 5V	

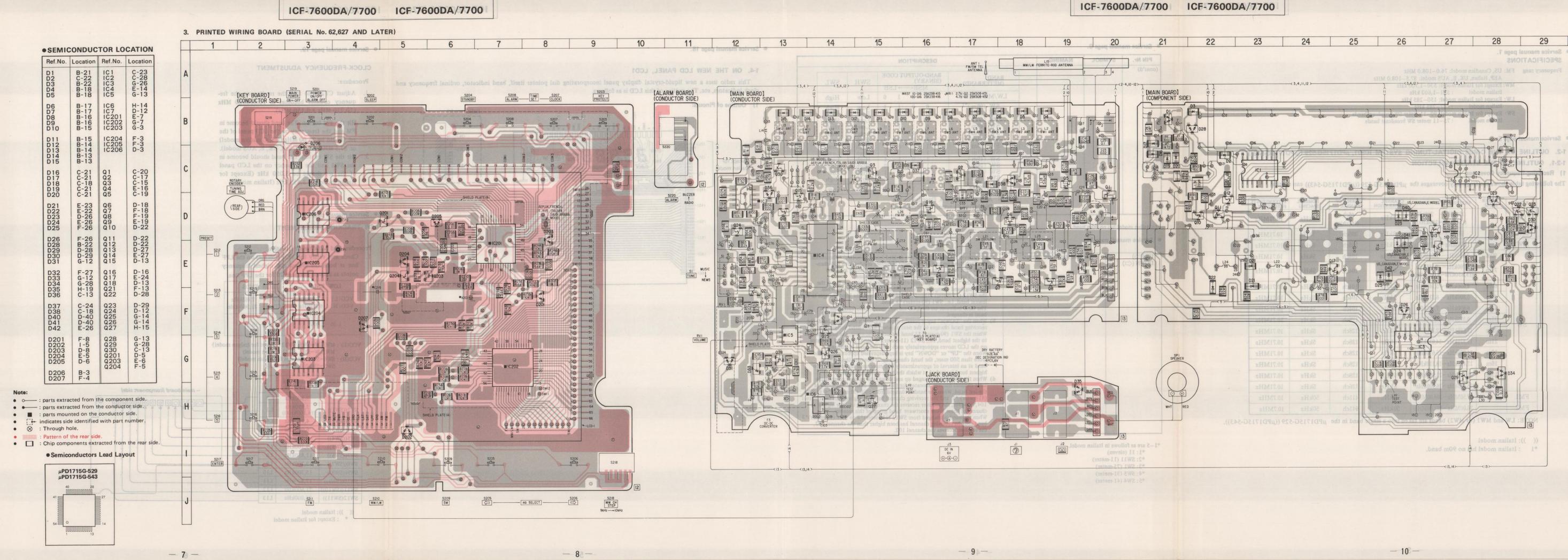
VCO(L): MW/LW~SW5, SW12 (Except for Italyan model)
MW/LW~SW4, SW11 (Italian model)

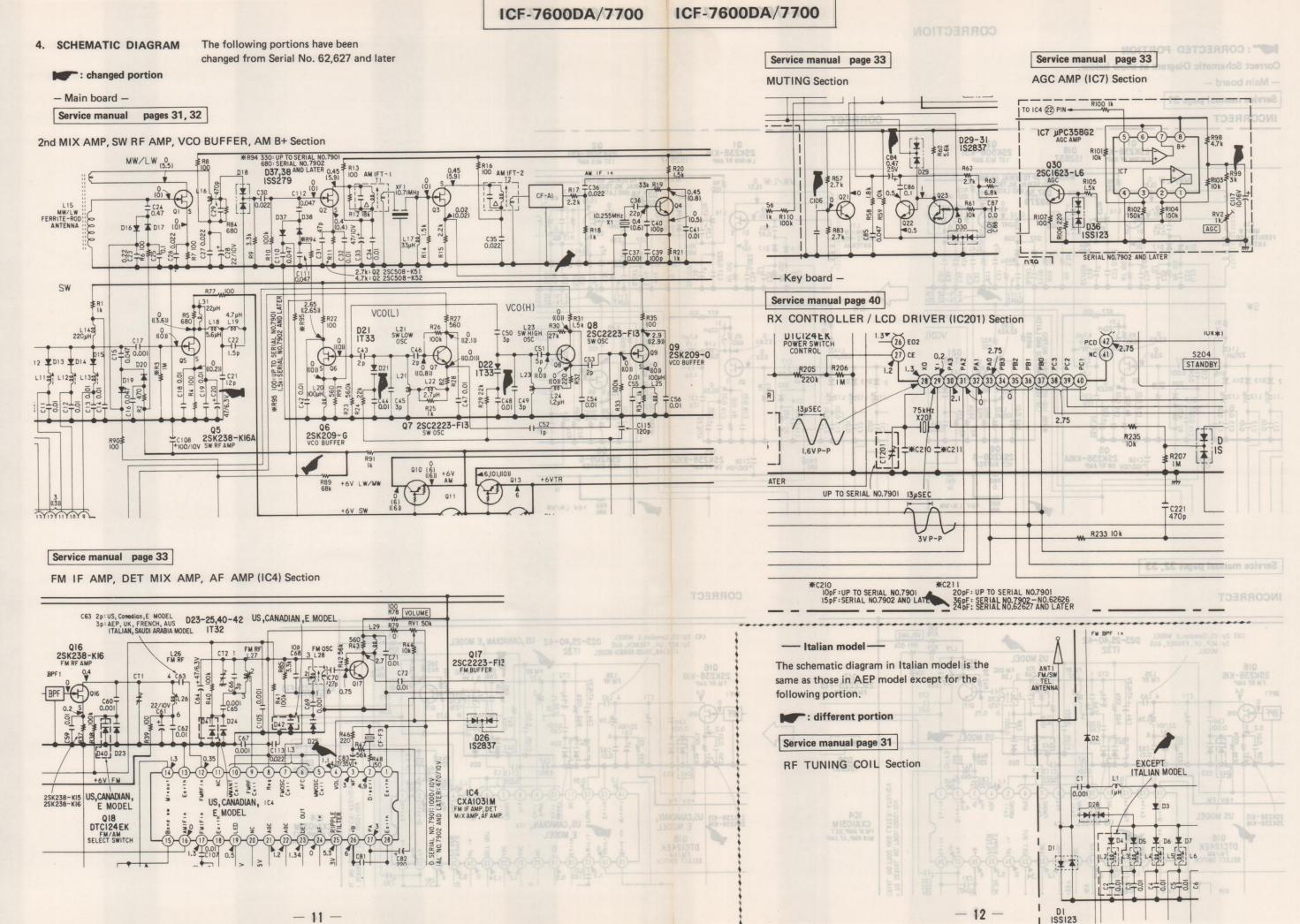
VCO(H): SW6~SW11 (Except for Italian model) SW5~SW10 (Italian model)



(()): Italian model

* : Except for Italian model





CORRECTION

: CORRECTED PORTION

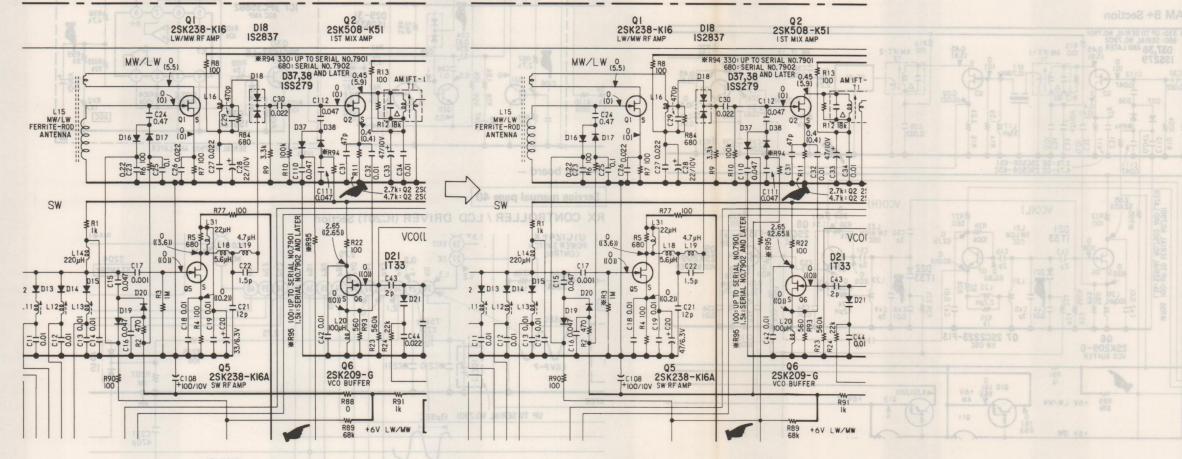
Correct Schematic Diagram as show below.

- Main board -

Service manual page 31

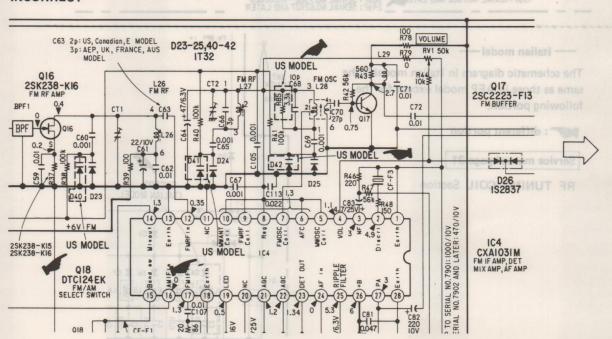
INCORRECT

CORRECT

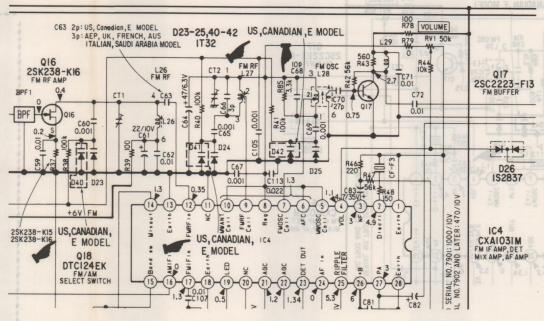


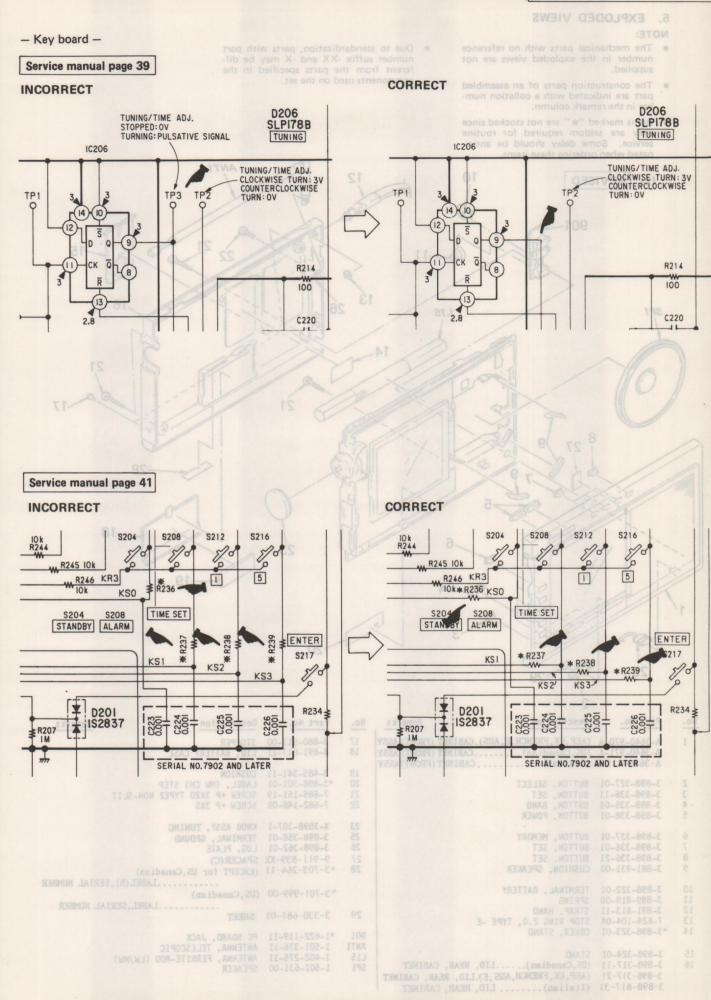
Service manual pages 32, 33

INCORRECT



CORRECT





5. EXPLODED VIEWS

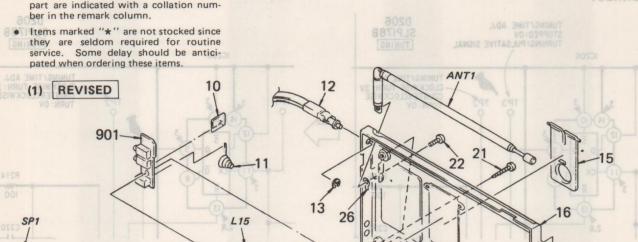
NOTE:

- The mechanical parts with no reference number in the exploded views are not supplied.
- The construction parts of an assembled part are indicated with a collation num-

Due to standardization, parts with part number suffix -XX and -X may be dif-ferent from the parts specified in the components used on the set.

Service menual page 39

- Key board -



		2	
No.	Part No.	Description Remarks	No.
1	A-3640-971-A	(AEP, UK, FRENCH, E, AUS). CABINET (FRONT) ASSY (US, Canadian) CABINET (FRONT) ASSY (Italian) CABINET (FRONT) ASSY	17 18
	A-3040-211-A	(Italian)Chbinei (Ironi)n331	19
2	3-898-327-01	BUTTON, SELECT	20
2 3 4 5	3-898-336-11	BUTTON, SET	21
4	3-898-335-01	BUTTON, BAND	22
5	3-898-338-01	BUTTON, POWER	23
6	3-898-337-01	BUTTON, MEMORY	25
6 7 8 9	3-898-336-01	BUTTON, SET	26
8	3-898-336-21	BUTTON, SET	27
9	3-881-931-00	CUSHION, SPEAKER	28
10	2 000 222 01	TERMINAL CATTERY	
11	3-898-322-01 3-889-819-00	TERMINAL, BATTERY SPRING	
12			00
		STOP RING 2.0, TYPE -E	29
14	*3-898-323-01	COVER, STAND	901

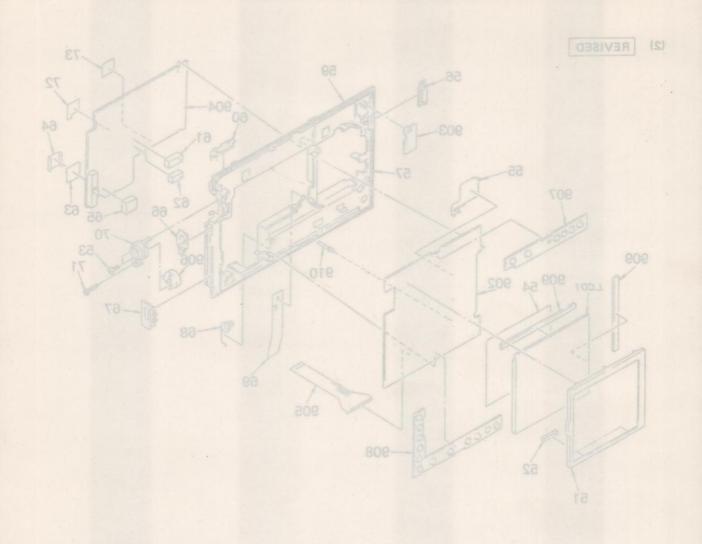
3-898-324-01 STAND
3-898-317-11 (US, Canadian).....LID, REAR, CABINET
3-898-317-21 (AEP, UK, FRENCH, AUS, E).LID, REAR, CABINET
3-898-817-31 (Italian)......LID, REAR, CABINET

No.	Part No.	Description Remarks
17	3-880-917-00	STOPPER
18	3-891-817-21	
19 20 21 22		CUSHION LABEL, (MW CH) STEP SCREW +P 3X20 TYPE2 NON-SLIT SCREW +P 3X5
23	X-3898-307-1	KNOB ASSY, TUNING
25	3-898-358-01	TERMINAL, GROUND
26	3-898-362-01	LUG, PLATE
27	9-911-839-XX	SPACER(C)
28	*3-703-264-11	(EXCEPT for US, Canadian)
		LABEL(B), SERIAL NUMBER
	*3-701-999-00	(US, Canadian)
		LABEL, SERIAL NUMBER
29	3-330-681-01	SHEET
901	*1-622-119-11	PC BOARD, JACK
ANT1	1-501-376-11	ANTENNA, TELESCOPIC
L15	1-402-275-11	ANTENNA, FERRITE-ROD (LW/MW)
SP1	1-502-631-00	

(2) REVISED		73
	56 59	72
		60904
	903	61 64
55		
907	57	
	S CONTRACTOR	62 66 65 63
00000		70
909	910	906 53 71
LCD1 909 54 902		10 000
		68
	7	
		69
	905	
600		
52	908	
51	7	

No.	Part No.	Description	Remarks	No.	Part No.	Description	Remarks
51	*3-898-333-01	HOLDER, LCD		70	*3-898-321-01	HOLDER	
52	3-898-349-01	CUSHION (A)		71	7-685-534-19	SCREW +B 2.6X8	
53	3-892-318-00	SCREW. SMALL		72		PLATE ASSY, SHIELD	
54	3-898-350-01	CUSHION (B)		73		PLATE (C) ASSY, SHIELD	
55					A 3070 300 1	THIL (C) INDI, DITTIE	
33	3-030-311-01	TEATE (D), SHILLD		902	A-3675-005-A	(US, Canadian, E)MOUNTED	PCB. KEY
56	3-898-318-01	(EXCEPT for 2E3)KNOB, SLIDE		302	A-3675-006-A		
50	3-898-318-21	(2E3)KNOB, SLIDE			*A-3675-008-A		
59	*3-898-316-01	CHASSIS			N 3073-000-N	(Italian)MOUNIED	PCB, KEY
60	3-898-318-11	KNOB, SLIDE		903	*1-622-121-11	PC BOARD, ALARM	
00	3-030-310-11	KNOD, SLIDE		904	A-3660-679-A		PCB. MAIN
61	*3 808 342 01	CASE (FM RF), SHIELD		301		(AEP, UK, FRENCH, AUS) MOUNTED	
62	*3-898-343-01	CASE (FM OSC), SHIELD				(Italian)MOUNTED	
63	*X-3898-305-1				- A-3000-743-A	(Italian)	rob, min
64	X-3898-304-1	PLATE (A) ASSY, SHIELD		905	1 622 125 11	PC BOARD, FLEXIBLE	
65	*3-898-345-01	CASE, SHIELD, D/D M		906	1-464-788-11		
00	~3-090-343-01	CASE, SHIELD, D/D M		900			000 2171
cc	2 000 210 01	KNOD TONE		-	1-571-057-11		
66	3-898-319-01			908	1-5/1-050-11	SWITCH, RUBBER KEY (S201-204,2	107,1007
67	3-898-320-01	KNOB, VOLUME		000	1 525 655 11	COMPLICTOR (COMPLETION)	
68	3-883-423-00	SPRING		909	1-535-655-11		
69	*3-898-341-01	CLOTH, DRAWER, BATTERY		910	1-535-666-11		
				LCD1	1-80/-///-11	(US, Canadian, E)	ORVETAL
						DISPLAY PANEL, LIQUID	CRYSTAL
					1-80/-//8-11	(AEP, UK, FRANCE, AUS)	
				mag		DISPLAY PANEL, LIQUID	CRYSTAL
			y Corpo	100	1-808-095-11		
			Audio Gro	1		DISPLAY PANEL, LIQUII	CRYSTAL
							00-7CE-6

9-952-684-81



				*3-898-333-01 3-898-349-00 3-892-318-00 3-898-350-01 *3-898-311-01	51 52 53 54 54 55
(US, Canadian, E)HOUNTED PCB, KEY (AEP, UK, FRENCH, AUS)HOUNTED PCB, KEY (Italian)MOUNTED PCB, KEY					
PC BOARD, ALARM (US,Canadian,E)HOUNTED PCB, MAIN (AEP,UK,FRENCH,AUS)HOUNTED PCB, MAIN (Italian)MOUNTED PCB,HAIN					
					66 68 69
	Son	y Corpor	ration		E

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Audio Group