TOSHIBA RF POWER AMPLIFIER MODULE

S-AV38

ORF POWER AMPLIFIER MODULE for VHF BAND

· for digital use

ABSOLUTE MAXIMUM RATINGS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V_{DD}	17	V
DC Supply Voltage	V_{GG}	7	٧
Input Power	Pi	17	dBmW
Operating Case Temperature Range	T _{c (opr)}	-30~100	°C
Storage Temperature Range	T _{stg}	-40~110	°C

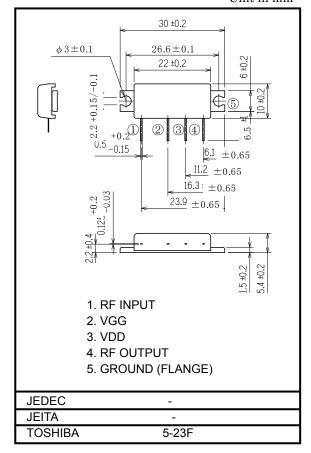
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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PACKAGE OUTLINE

Unit in mm



Weight:3.5g

ELECTRICAL CHARACTERISTICS (Tc = 25°C, $Z_G = 50 \Omega$)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Frequency Range	f _{range}	_	260	_	266	MHz
Output Power	Ро	V_{DD} = 7.2V, Po=35dBmW(Pi=adjust) I_{DD} =1.7A(V_{GG} = adjust) , Z_L = 50 Ω After that Pi = 15dBmW	38.8	_	_	dBmW
Input Power	Pi	V_{DD} = 7.2V, I_{DD} = 1.7A (V_{GG} = adjust) Po = 35dBmW(Pi=adjust), Z_L = 50 Ω	_	_	5	dBmW
Gate Bias Voltage	V _{GG}	V_{DD} = 7.2V, I_{DD} = 1.7A (V_{GG} = adjust) Po = 35dBmW(Pi=adjust), Z_L = 50 Ω	2.5	_	3.5	V
Gate Bias Current	I _{GGBias}	V_{DD} = 7.2V, I_{DD} = 1.7A (V_{GG} = adjust) Po = 35dBmW (Pi= adjust), Z_L = 50 Ω After that Pi OFF	_	_	1	mA
Adjacent-Channel Power Ratio	ACP	$\begin{aligned} &V_{DD} = 7.2 \text{V, } I_{DD} = 1.7 \text{A (V}_{GG} = \text{adjust)} \\ &\text{Po} = 35 \text{dBmW (Pi= adjust), } Z_L = 50 \Omega \\ &\text{Modulated Wave : } \pi / 4 \cdot \text{DQPSK} \\ &(\alpha = 0.5, 32 \text{kbps}) \\ &\text{Band Width : } 16 \text{kHz} \\ &\text{Frequency Offset : } 25 \text{kHz} \end{aligned}$	_	_	-35	dB
Second Harmonic	2nd HRM		_	_	-27	dB
Third Harmonic	3rd HRM	V_{DD} = 7.2V, I_{DD} = 1.7A (V_{GG} = adjust) Po = 35dBmW (Pi= adjust), Z_L = 50 Ω	_	_	-30	dB
Harmonic	HRM		_	_	-35	dB
Rate of Adjustment for Input Load	VSWRin	Input VSWR (When RF output pin connects 50Ω Load)	_	_	3	_
Rate of Adjustment for Output Load	VSWRout	Input VSWR (When RF input pin connects 50Ω Load)	_	_	2.5	_
Ralative Phase Variation		V_{DD} = 7.2V, I_{DD} = 1.7A (V_{GG} = adjust) Po = 5 to 35dBmW (Pi= adjust) Z_L = 50 Ω (@ Po = 35dBmW)	_	_	±12	o
Load Mismatch	_	V_{DD} = 7.2V, I_{DD} = 1.7A (V_{GG} = adjust) Po = 35dBmW (Pi= adjust, Z_L = 50 Ω) VSWR LOAD 20: 1 ALL PHASE	No Degradation		_	
Stability	_	V _{DD} = 6.0 to 9.0V, V _{GG} = 1 to 5V Pi = -40 to 13 dBmW VSWR LOAD 3: 1 ALL PHASE	All spurious output than 60dB below desired signal		_	

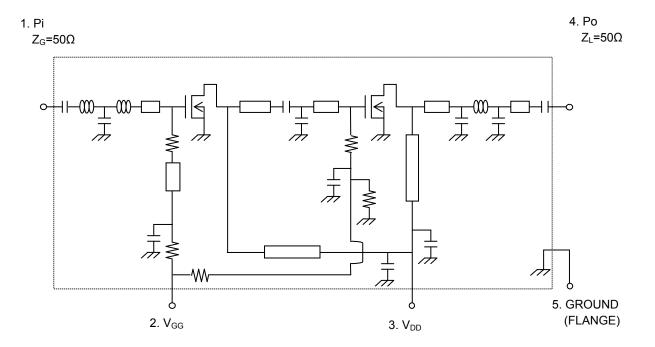
Caution

• This product has intersetting cap. Please pay attention for exceeding stress and foreign matter in your application. And not to take away the cap.

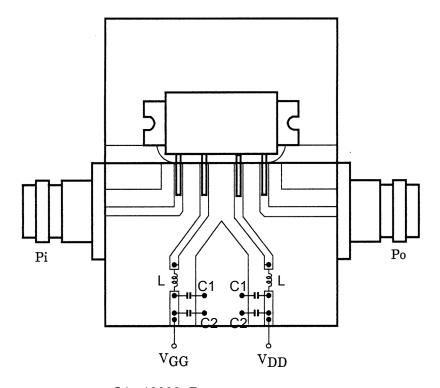
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- Do not break, cut, crush or dissolve chemically. Dispose of this product properly according to law.
 Do not intermingle with normal industrial or domestic waste.
- · This product is electrostatic sensitivity, please handle with caution.

SCHEMATIC



TEST FIXTURE



C1 : 10000pF C2 : 10 μ F

L : ϕ 0.8 ENAMEL WIRE 8T 5ID

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2007-11-01

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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