The RF Line NPN Silicon RF Power Transistor

Designed for 12.5 volt low band VHF large—signal power amplifier applications in commercial and industrial FM equipment.

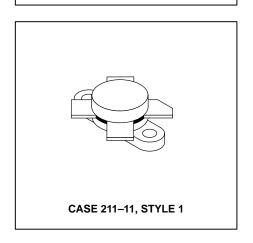
- Specified 12.5 V, 50 MHz Characteristics —
 Output Power = 70 W
 Minimum Gain = 11 dB
 Efficiency = 50%
- · Load Mismatch Capability at High Line and RF Overdrive

MRF492

70 W, 50 MHz RF POWER TRANSISTOR NPN SILICON

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	18	Vdc
Collector-Base Voltage	VCBO	36	Vdc
Emitter-Base Voltage	V _{EBO}	4.0	Vdc
Collector Current — Continuous	IC	20	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	250 1.43	Watts W/°C
Storage Temperature Range	T _{sta}	-65 to +150	°C



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (2)	$R_{\theta JC}$	0.7	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = 100 mAdc, I _B = 0)	V(BR)CEO	18	_	_	Vdc
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, V _{BE} = 0)	V(BR)CES	36	_	_	Vdc
Emitter–Base Breakdown Voltage (I _E = 10 mAdc, I _C = 0)	V(BR)EBO	4.0	_	_	Vdc
Collector Cutoff Current (V _{CE} = 13.6 Vdc, V _{BE} = 0)	ICES	_	_	20	mAdc
ON CHARACTERISTICS					
DC Current Gain (I _C = 5.0 Adc, V _{CE} = 5.0 Vdc)	hFE	10	_	150	_
DYNAMIC CHARACTERISTICS					
Output Capacitance (V _{CB} = 15 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	275	450	pF
FUNCTIONAL TESTS					
Common–Emitter Amplifier Power Gain (V _{CC} = 12.5 Vdc, P _{Out} = 70 W, f = 50 MHz)	G _{PE}	11	13	_	dB
Collector Efficiency (V _{CC} = 12.5 Vdc, P _{out} = 70 W, f = 50 MHz)	η	50	_	_	%

NOTES:

- 1. These devices are designed for RF operation. The total device dissipation rating applies only when the devices are operated as RF amplifiers.
- 2. Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.



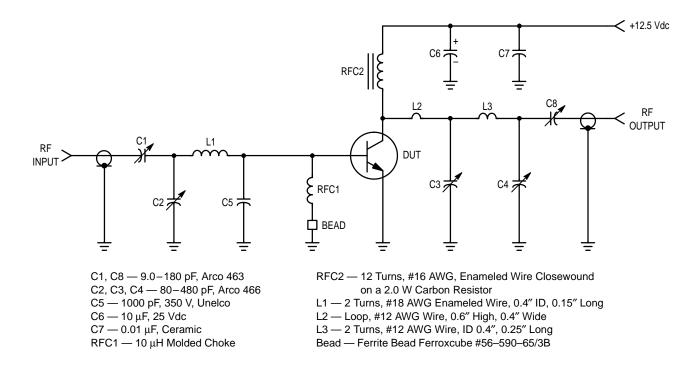


Figure 1. 50 MHz Test Circuit

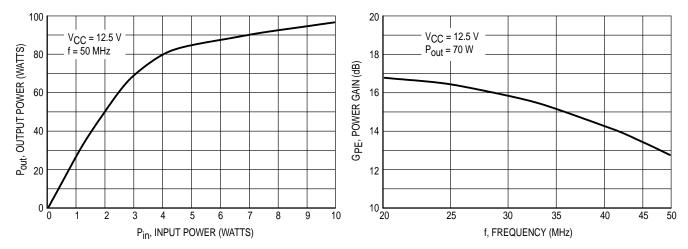


Figure 2. Output Power versus Input Power

Figure 3. Power Gain versus Frequency

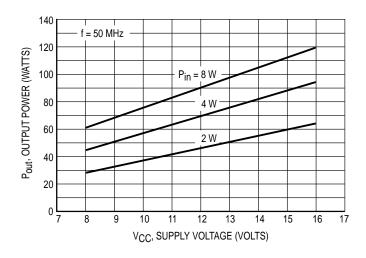


Figure 4. Output Power versus Supply Voltage

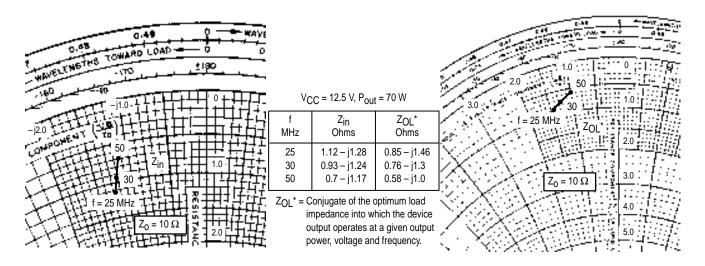
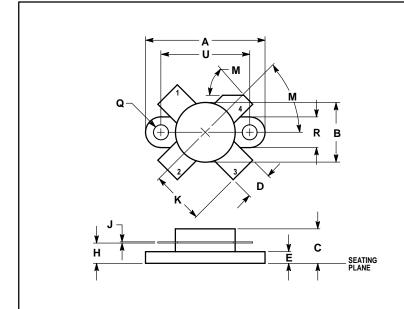


Figure 5. Series Equivalent Input/Output Impedances

PACKAGE DIMENSIONS



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.960	0.990	24.39	25.14	
В	0.465	0.510	11.82	12.95	
С	0.229	0.275	5.82	6.98	
D	0.216	0.235	5.49	5.96	
Е	0.084	0.110	2.14	2.79	
Н	0.144	0.178	3.66	4.52	
J	0.003	0.007	0.08	0.17	
K	0.435		11.05		
М	45 °NOM		45 ° NOM		
Ø	0.115	0.130	2.93	3.30	
R	0.246	0.255	6.25	6.47	
U	0.720	0.730	18.29	18.54	

STYLE 1: PIN 1. EMITTER

- 2. BASE
- 4 COLLECTOR

CASE 211-11 ISSUE N

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